

Intel® Smart Connect Technology

Platform Design Specification

April 2014

Revision 1.8

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Revision History

Document Number	Revision Number	Description	Revision Date
507302	0.5	<ul style="list-style-type: none">Initial release based upon final 3.0 spec (1.0)Added GAWD/SAWD ACPI methods (section 4.1.2.3)Removed ASL sample code as BIOS sample code provided separatelyUpdated Registry Keys (section 6.2.4) and OEM setting methodAdded Remote Wake requirements - denoted by (RW)Removed section describing turning off LEDs as a recommendation as it is a requirementMaximum thermal back off is 1 hour (section 7.2.4)Removed duty cycle and battery back off restrictions (section 6)Added requirement to assert PCIe PERST# while in S3/S4 (section 3.2)	September 2012
	0.6	<ul style="list-style-type: none">Corrected section 4.4.1 about power button handling in S0-ISCT for systems without EC and system with ECUpdated Wake Duration definitions (section 6.2.2)Moved programming information to separate document (section 6)Covers both 4.0 and 4.1 releases	November 2012
	0.7	<ul style="list-style-type: none">Updated RW BIOS changes to indicate their usage to allow the OEM to decide if they wish to use them (section 44.1 and .1.1.1)Added note about EC timer drift acceptability (section 4.1.1.3)Updated registry settings with Thermal Thread Threshold Values (section 6.2.4)	December 2012
	1.0	<ul style="list-style-type: none">Updated DeepSx support Intel® Series 8 Chipset (section 2.3)Updated RF Kill switch handling when platform in S3 (section 5.2.4)Added display off during S0-ISCT handling for eDP panels and external displays (section 4.2)Added touch panel S0-ISCT handling (section 4.3)Updated Table 8-1 to indicate Remote Wake capabilities with WLAN and ME WLAN Provisioning states (section 8.2)Added note about enabling native PCI Express* for proper NetDetect functionality (section 2.2)Changed requirement for RW wake-once-a-day (section 4.1.1.1)Updated EC wake timer requirements (section 4.1.1.3)Updated DCBatteryThreshold usage setting (section 6.2.3)	March 2013



Document Number	Revision Number	Description	Revision Date
	1.1	<ul style="list-style-type: none">• Clarified wording about WLAN Sx power requirements. WLAN power is enabled in Sx only when Agent tells BIOS to enable it. Power is not required always.• Clarified power button handling during S0-ISCT (section 4.6.1.1)• Corrected Broadcomm* NetDetect supported SKU (section 2.1)• Made note of valid internet connection required or NetDetect will be enabled (section 6.2.2)• Updated registry settings available (section 6.2.4)• Added note about GPCS Lid State indicator returning '1' when platform in tablet mode (section 4.1.2.3.12)• Added DeepSx PCH configuration registry programming to ensure NetDetect functionality (section 2.3 and 4.1)	May 2013
	1.2	<ul style="list-style-type: none">• Updated power button event handling for systems with EC in S0-ISCT (section 4.6.1)• Added note that state of RF Kill must not change upon platform transition from S3 to S0 if the wake reason was a PME wake for NetDetect (section 3.2)• Updated OEM INI file Whitelist description (section 6.4.2.4)• Updated with Bay Trail M/D design guidelines	June 2013
	1.3	<ul style="list-style-type: none">• Updated GPWR bits (section 4.1.2.3.10)• Updated display on/off setting during Sx resume (section 4.2.2)	June 2013
	1.4	<ul style="list-style-type: none">• Added _CID creation (section 4.1.2.2)	August 2013
	1.5	<ul style="list-style-type: none">• Added section on Remote Wake Housekeeping Service (section 6.2.4)	September 2013
	1.6	<ul style="list-style-type: none">• Added new information for supporting iSCT 5.0 (section 2.3)• Updated wake once a day usage (section 4.1.1.1)• Updated OS S4 wake support (section 4.1.2.4.1, 4.1.2.4.2, 4.2.2.4.3, 4.1)• Changed Battery Threshold default to 30% from 15% (section 6.2.5)• Updated LED indicator state to include power button indicator if applicable on platform• Added information about BIOS/EC requirement to supply power to WLAN card when BIOS transitions platform to Rapid Start S4 from S3 state and WLAN card needs power for NetDetect (section 2.5)	October 2013
	1.7	<ul style="list-style-type: none">• Added note to clear PME wake reason to prevent wake bounce when system goes back to S3 (section 2.2 and 4.1)• Clarified SMBIOS Type-24 Table requirement when BIOS has Power On or Administrative password implemented. Previous versions were incorrect in indicating "disk" password which is not part of the SMBIOS Type-24 table (section 2.3, 4.1)• Added Section 2.4 to describe Remote Wake from OS S4 (Hibernate) requirements and exceptions	November 2013



Document Number	Revision Number	Description	Revision Date
	1.8	<ul style="list-style-type: none">Added new bit values for GPCS and SAOS related to support of Wake from DC to AC(section 4.1.2.4.12, 4.1.2.4.2). Added section 2.8 to explain Wake from Power Source Change	April 2014

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1 Introduction

1.1 Purpose of this Document

This platform design specification provides instructions and recommendations for integrating the Intel® Smart Connect Technology (ISCT) into a platform. The intent of this document is to describe the hardware, BIOS, and EC changes required for a platform. In addition to the platform requirements, the required software components and their configuration are included along with system testing to verify Intel® Smart Connect Technology is working correctly.

Intel® Smart Connect Technology is a feature of the platform in which the software on the platform and combination of NIC (LAN/WLAN/WWAN) features provides content updates during periods of PC in-activity. These can be categorized as:

- Always On/Always Updated:
 - Intel® Smart Connect Technology Agent schedules platform to wake up from S3 periodically to allow network applications to obtain new data (email updates, social media applications ...) and then transitions back to S3.
 - Power friendly wake durations:
 - Standard Wake Duration if the platform is connected to battery. Wake duration is limited to maximum of 165 seconds to conserve battery power.
 - Extended Wake Duration if the platform is connected to AC. Extended wake duration allows for larger content download. Once network activity falls below 100KB for 10 seconds, the platform is transitioned back to S3.
 - During wakeup, Intel® Smart Connect Technology OS Service (Agent) places platform into a lower power S0 state (e.g., panel turned off, CPU in lowest P-state). This state can be referred to as S0-ISCT.
 - Factors of thermal considerations and amount of data to update factor into "Always Updated" period of activity.
- Intel Energy Efficient Always On Connectivity (EE-AOC) – mobile only:
 - WLAN running in S3 (AC/Battery) with NetDetect FW allows NIC to scan for WiFi networks that match a configured profile list and if a match is found, the platform wakes to S0-ISCT to get connected and update content.
 - Intel WWAN running in S3 (AC/Battery with Layer 2/3 connectivity that maintains connectivity to service provider (no data transfer)
- Remote Wake:
 - Allows platform in S3 / S4 sleeps states to be woken by remote application software. Once local application is done, platform goes back to existing sleep state.

For mobile platforms, special attention to thermal monitoring and control is defined to ensure safety and reliability for systems confined to areas where thermals may rise



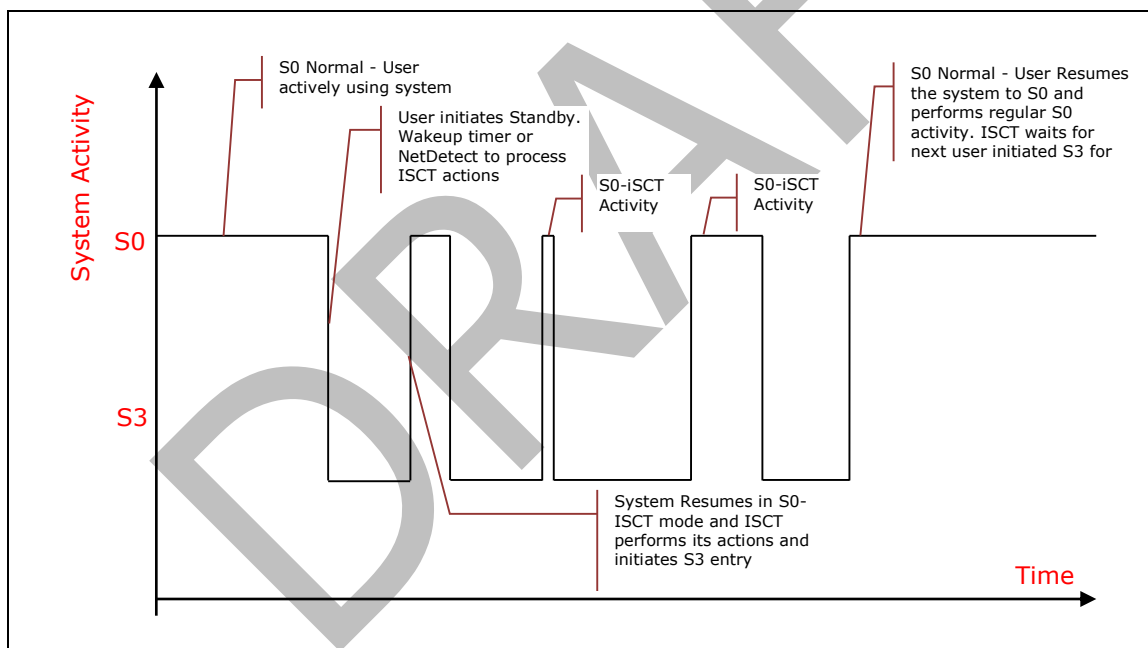
unexpectedly due to insulating qualities of the environment (e.g. operation in a book bag or briefcase).

1.2 Intel® Smart Connect Technology Life Cycle

The Intel® Smart Connect Technology Agent, once installed and configured to be active, periodically wakes up the system from S3 and performs user state gathering and initiates re-entry to S3 to wake up after a set time interval or based on network presence. For RTC wake events enabled by the user or other software residing on the platform, those wake events will not be run in the S0-ISCT mode if the BIOS sets the wake reason to indicate a no S0-ISCT mode RTC wake.

An example time sequence diagram shown in and the subsequent sections outline a typical SCT cycle and highlight key areas of concerns. Recommendations for handling these concerning areas are highlighted as well. Figure 1-1 illustrates SCT behavior and the subsequent sections outline a typical SCT cycle and highlight key areas of concerns. Recommendations for handling these concerning areas are highlighted as well.

Figure 1-1. Typical Intel® Smart Connect Technology Activity





1.3 Potential Platform Thermal Concerns

During anytime the system is in S0, the system is actively consuming power and dissipating heat with fans on in a user present. However, the system environment during the automatic S0-ISCT mode is unknown. The system might be inside a bag inside a car or might be at the user's desk in idle mode with the lid open. The thermal constraints (maximum allowed temperature for various components and the chassis; fan usage constraints etc.) for the different environments varies drastically, making it necessary to take unattended (automatic) S0-ISCT wake into system design Consideration. Refer to Chapter 7, [Thermal Considerations](#) for more information on thermal considerations.

1.4 Reference Documents

Document	Document Number
ACPI / Power Management	http://www.acpi.info/
ACPI / Power Management in Microsoft Operating Systems	http://www.microsoft.com/whdc/system/pnppwr/powermgmt/default.mspx
Broadwell Platform Design Guide	XXXXXX
Broadwell Platform Power Delivery Design Guide	XXXXXX
RS – Intel® Series 9 Chipset Family External Design Specification (EDS)	XXXXXX
PCI Express* Base Specification Rev. 2.0	www.pcisig.com
RS – Intel® Rapid Start Technology BIOS Specification 0.6.1	30258
Intel® Smart Connect Technology Compliancy Test Plan	507304
Intel® Smart Connect Technology Installation and Troubleshooting Guide	507303
Intel® Smart Connect Technology Programming Guide	512222
SMBIOS Specification	http://dmtf.org
Bay Trail M/D Platform Design Guide	512238
Bay Trail M/D SoC External Design Specification	512177



1.5 Terminology

Term	Description
Agent	Intel® Smart Connect Technology OS Service
CRB	Customer Reference Board
CRV	Chief River
EC	Embedded Controller/Keyboard Controller
EE-AOC	Intel Energy Efficient Always On Connectivity
Intel® ME	Intel® Management Engine
Intel® TXE	Intel() Trusted Execution Engine
ISV	Independent Software Vendor
NIC	Network Interface Card or Network Interface Controller
RTC	Real Time Clock
RW	Remote Wake
SCT, ISCT	Intel® Smart Connect Technology
S0-ISCT	Reduced S0 power model that the Intel® Smart Connect Technology Agent runs to update content
Wireless Local Area Network (WLAN)	A local area communications network based on wireless technology
Wireless Wide Area Network (WWAN)	A wide area communications network based on cellular technology

§



2 Platform Requirements

2.1 Component List

The following components are required to support Intel® Smart Connect Technology on a platform:

Table 2-1. Required Components

Item	Supplier
CPU/PCH: Intel® CPU (Atom, Celeron, Pentium, Core) and any PCH SKU	OEM
Windows* 7 SP1 / Windows* 8 / 8.1 Platform	OEM
NetDetect Compatible WLAN Card <ul style="list-style-type: none"> • 6300 (Puma Peak) • 6250 (Kilmer Peak) • 6235 (Jackson Peak 2) • 6205 (Taylor Peak) • 2230 (Jackson Peak 1) • 2200 (Marble Peak) • 3160 (Wilkins Peak 1) • 7260 (Wilkins Peak 2) • 4352 Broadcom* 	OEM
Intel® Smart Connect Technology Agent	Intel
UI for Intel® Smart Connect Technology Agent configuration	Intel or OEM
Intel® Smart Connect Technology Agent Configuration Applet Dll	Intel
HW/BIOS/EC Changes (see notes in respective sections)	OEM
Intel® Smart Connect Technology Agent Configuration	OEM
Radio On/Off Switch (optional)	OEM
Remote Wake enabled applications (optional)	OEM

The following components are optional to support Intel® Smart Connect Technology on a platform:

Table 2-2. Optional Components

Item	Supplier
WWAN (3G) NIC with L2/L3 Connectivity in S3 if WWAN supported (optional)	Intel WWAN NIC
Intel LAN for Remote Wake over LAN (only Intel LAN devices supported by Intel® ME for Remote Wake capability)	Intel



2.2 Checklist

The following checklist provides a list of required tasks to assist the integration and ensure all required Intel® Smart Connect Technology integration steps are completed. Additional tasks of validation and manufacturing preparation are not included.

Item	✓
Windows* 7 SP1 / Windows* 8.x Platform	
BIOS ACPI IAOE control method	
S3 (Intel® Rapid Start Technology S4) power to WLAN NICs used for Always Connected in AC/Battery <ul style="list-style-type: none">Power controlled by BIOS/EC using on/off state specified by SWLS ACPI Control Method regardless of Intel® ME configuration	
Display handling during S0-ISCT support integrated (see Section 4.2 - Display Handling for more information)	
For systems with EC Timer capability (required for Mobile): <ul style="list-style-type: none">EC Timer implementedEC Timer only programmed in S3 and OS S4Wake-once-a-day Remote Wake Timer	
For systems with RTC Timer only capability (Systems with no EC): <ul style="list-style-type: none">RTC Timer implementedRTC Timer only programmed in S3 and OS S4Wake-once-a-day Remote Wake Timer	
PCI Express* Wake Disable (PCIEXP_WAKE_DIS) bit 14 in PM1_EN PCH register disabled (0b)	
The WAKE# Pin DeepSx Enable (WAKE_PIN_DSX_EN) bit (bit #2) set to '1' in the DeepSx Configuration (DSX_CFG) PCH configuration.	
Native PCI Express* Support enabled for proper NetDetect Functionality	
Clear PCI Express* Wake Status (PCIEXPWAK_STS) bit 14 in PM1_STS PCH register upon wake to prevent "bounce" or wake happening again after platform goes back to S3.	
All LED indicators maintain S3 state when platform is in S0-ISCT. This includes the power button indicator if used on platform	
Thermal runaway protection in event of skin temperature exceeded for notebook in bag (optional)	
NetDetect capable WLAN Driver (required for Mobile)	
Intel LAN device for Remote Wake and Intel® ME configured to support	
Intel® Smart Connect Technology SW installed	
Antennae design allows minimal to no attenuation closed lid RF reception	



Item	✓
Windows* Hybrid Sleep Disabled (prevents saving memory image to disk on every transition to S3 for periodic wake)	

2.3 Wake from OS S4 (Hibernate)

Note: This feature is new for 5.0 version

Microsoft* Windows 8/8.1 enables OS S4 (Hibernate) after S3 of 3 hours by default in the various versions. Because Intel® Smart Connect Technology does not wake during Extended Hours, the platform will no longer wake at dawn to update content since the extended hours sleep duration exceeds the OS S4 Hibernate timer and platform will be in OS S4

To allow the wake at dawn feature of Smart Connect Technology, support for timer wake from OS S4 hibernate is present.

Scenarios:

- If user puts system to S3 and the OS Hibernate timer kicks in, system will enter S4. In this case, Smart Connect Technology will schedule a wake at dawn. After system wakes at dawn, ISCT will put system back to S3 instead of S4.
- If user puts system to S4, Smart Connect Technology will schedule a wake at dawn. After system wakes at dawn, Smart Connect Technology will put system back to S4 instead. If there is no user intervention, system will be scheduled to wake at next morning.
- If no Internet connection prior to OS S4 entry, NetDetect is not configured and wake at dawn is scheduled.

BIOS/EC support is required to support the wake from OS S4. Please refer to the BIOS section for details.

- Support checking for Smart Connect Technology timer wake from OS S4 and updating GPWR to indicate timer wake event
- GABS Bit '7' indicates of platform supports wake from OS S4
- Implement SMBIOS Type-24 table if BIOS managed password for Power On or Administrative
- Report HDD password enabled by setting Power On BIOS password field in the SMBIOS Type-24 table
- Implement SAOS Bit '2' to return GAOS Bit '2' indication if BIOS allows wake from OS S4 on S4 transition
- Suppress fan speed to reduce the acoustic noise when platform wakes from OS S4



- Implement an OS S4 watchdog timer and event handler in case of PBA (Pre Boot App). The event handler cancels the watchdog timer when it receives the GPWR ACPI call. If the watchdog timer is not cancelled in 5 minutes, BIOS puts system back to S4. This is only used if SAOS=1 and the S4 wake reason was for Smart Connect Technology timer.

Exceptions:

Periodic Wake from OS S4 is not supported if the Agent finds any of the following on the platform:

- SMBIOS Type-24 Table reports Administrative BIOS Password enabled
- SMBIOS Type-24 Table reports Power On BIOS Password enabled
- The ATA Identify Device command reports Security is enabled on the hard disk
- Hard disk is encrypted by encryption software

2.4 Remote Wake from OS S4 (Hibernate)

To allow Remote Wake from OS S4 feature of Smart Connect Technology, support for timer wake from OS S4 hibernate is present.

BIOS support is required to support Remote Wake from OS S4.

- Support checking for PME# Wake from OS S4 and updating GPWR to indicate PME Wake event if the wake was from the LAN/WLAN card
- Implement SMBIOS Type-24 table if BIOS managed password for Power On or Administrative

Exceptions:

Remote Wake from OS S4 is not supported if the Agent finds any of the following on the platform:

- SMBIOS Type-24 Table reports Administrative BIOS Password enabled
- SMBIOS Type-24 Table reports Power On BIOS Password enabled
- The ATA Identify Device command reports Security is enabled on the hard disk
- Hard disk is encrypted by encryption software

2.5 DeepSx Coexistence Considerations

Note: This section is not applicable to Bay Trail M/D

For platforms using the Intel® Series 8 Chipset, implementing DeepSx and Intel® Smart Connect Technology together, the platform will need to enable the PME_WAKE# from DeepSx to allow NetDetect to work properly when the platform is in DeepSx.



Since the Wake pin of the Intel® Series 8 Chipset is not available when the platform is in DeepSx, the PME_WAKE# signal can be routed to the EC and the EC power on the chipset and then signal the Chipset wake pin.

The WAKE# Pin DeepSx Enable (WAKE_PIN_DSX_EN) bit (bit #2) must be set to '1' in the DeepSx Configuration (DSX_CFG) PCH configuration register for NetDetect to work from DeepSx.

Upon wake, clear the PCI Express* Wake Status (PCIEXPWAK_STS) bit 14 in PM1_STS PCH register upon wake to prevent "bounce" or wake happening again after platform goes back to S3.

Consult the Intel® Series 8 Chipset documentation for more information on this.

2.6 Intel® Rapid Start Technology Coexistence Considerations

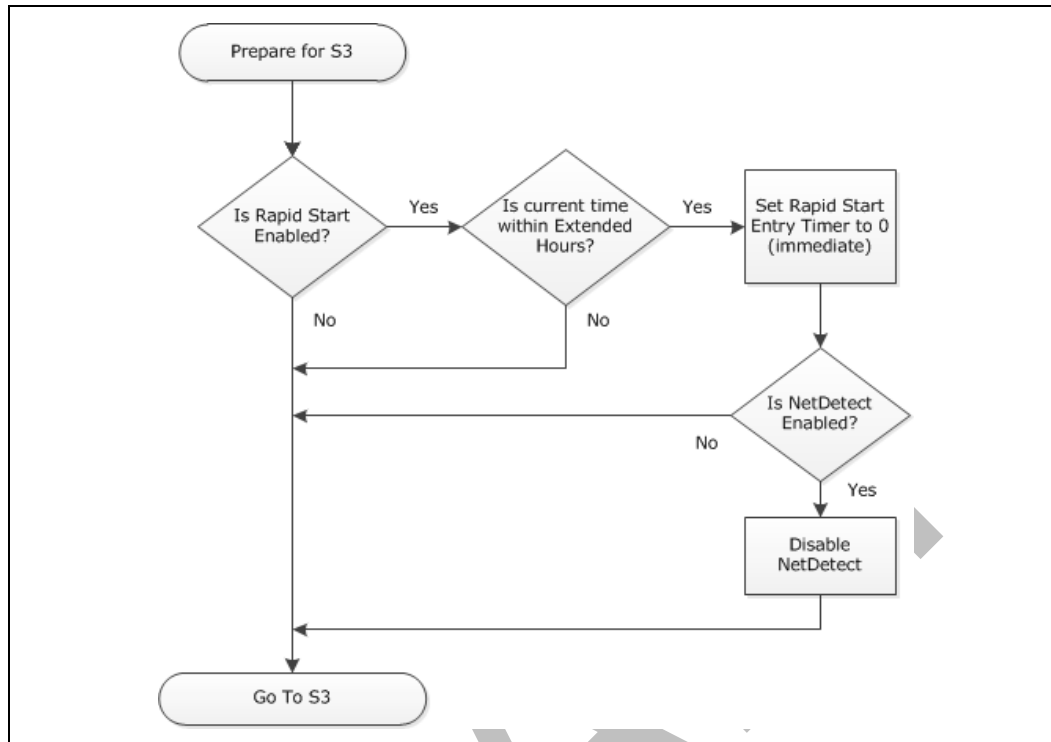
Note: This section is not applicable to Bay Trail M/D

Intel® Rapid Start Technology is a technology that addresses the desire for greater system responsiveness and lower power consumption during sleep states. Intel Rapid Start Technology snapshots system memory state on S3 (standby), transfers to SSD and transitions platform to S4 providing a zero power standby state. Upon power-on, restores non-zero system memory from SSD and resumes OS from S3 state.

Currently on platforms where Intel® Smart Connect Technology and Intel® Rapid Start Technology coexist, the Intel® Smart Connect Technology Agent in off hours (Extended Power Saving) checks if Intel® Rapid Start Technology is enabled on the platform (active), and if it is, programs The Rapid Start Entry timer with a value of 0 min (so that the platform will go to Rapid Start mode immediately upon transition to S3). If Intel® Rapid Start Technology is not enabled (active); no changes are made to the Intel® Rapid Start Technology settings. During non-Extended Power Savings hours, no change is made to the Intel Rapid Start Technology settings. For the case where Intel® Smart Connect Technology is disabled by end user or via the BIOS, The Intel® Smart Connect Technology Agent will not enable Intel® Rapid Start Technology. The Intel® Smart Connect Technology communicates settings thru the Intel® Rapid Start Technology Driver.

The following figure illustrates the interaction with Intel® Rapid Start Technology as the Intel® Smart Connect Technology prepares to enter S3.

Figure 2-1. Intel® Rapid Start Technology Interaction



If Intel® Rapid Start Technology is enabled on the target Intel® Smart Connect Technology platform and enabled via [registry settings](#); the following is a list of design requirements:

- S4 AC/Battery power to the WLAN NIC (PCIe*) (mobile only)
- BIOS/EC enable power to WLAN NIC when it transition system to Rapid Start S4 from S3 state and SWLS ACPI method has indicated power is to be supplied to mini-card in S3 state
- PCIe* wake event support for Net Detect in S4 for both AC and Battery modes (mobile only)
- BIOS follow the requirements of handling an existing RTC clock setting. Consult the Intel® Rapid Start Technology BIOS Specification for more information

Note: Refer to [Section 4.1](#) for BIOS requirements to address any contention amongst various timers.

Throughout this document Intel® Rapid Start Technology integration points are indicated, to assist in Intel® Smart Connect Technology integration on a platform that is Intel® Rapid Start Technology enabled.

2.7 Intel® Smart Response Technology Coexistence

Intel® Smart Connect Technology uses a feature of Intel® Smart Response Technology to reduce chances of HDD shock in a non-SSD platform. If the HDD in the platform



support PUIS (Power-Up In Standby), the drive will only spin-up when the spin-up ATA command is sent to the drive.

Upon resume from S3, if the platform enters S0-ISCT, the Intel® Smart Connect Technology Agent will instruct the Intel® Smart Response Technology driver to not spin-up the drive for the time of the S0-ISCT session unless there is a cache miss and the Intel® Smart Response Technology driver is required to spin-up the drive. If the platform does not enter S0-ISCT or a user event exits the platform from S0-ISCT, then the HDD is spun up.

2.8 Wake system on power source change from DC to AC when ISCT is enabled

The Intel® Smart Connect Technology Agent will wake the system when DC is the power source when the system was put into S3/S4 and AC power is plugged in while the system is in S3/S4. The following ACPI calls are required:

- SAOS.bit0 = 1 and SAOS.bit3 = 1. If system is in S4, check SAOS.bit2=1

If BIOS/EC supports wake from power source change, BIOS sets GPCS.bit1 = 1. The Intel® Smart Connect Technology Agent programs SAOS.bit3 if GPCS.bit1 = 1 when wake from power source change is desired.

- At suspend, EC memorizes the power source if SAOS.bit0 = 1 and SAOS.bit3 = 1.
- If the power source at suspend time is DC, EC monitors the power source change.
 - ✓ When power source changed from DC to AC in S3, EC wakes system and set the wake reason as ISCT wake.
 - ✓ When power source changed from DC to AC in S4, EC wakes system and set the wake reason as ISCT wake if SAOS.bit2 = 1.
 - ✓ BIOS should set GPWR.bit1 = 1 as the wake reason.

If the power source at suspend is AC, EC does not wake the system on power source change.



3 Hardware

Note: This section covers HW related design requirements that affect mobile platforms only or platforms that have a detachable component that contains the WLAN component

3.1 Antennae Design

One usage model of Intel® Smart Connect Technology is ability to receive content updates when the lid of a notebook is closed with WLAN connection. To support this usage model, RF attenuation of the WLAN antennae needs to be minimized to allow periodic wake content updates via WLAN with the lid closed. The amount of acceptable RF attenuation is a platform designer choice – our requirement is for platforms to place the antennae in a location that does not cause attenuation with the lid closed.

3.2 Platform WLAN NIC Sx Requirements

For each Intel® Smart Connect Technology NIC supported (WLAN NIC, the following hardware support is **required**:

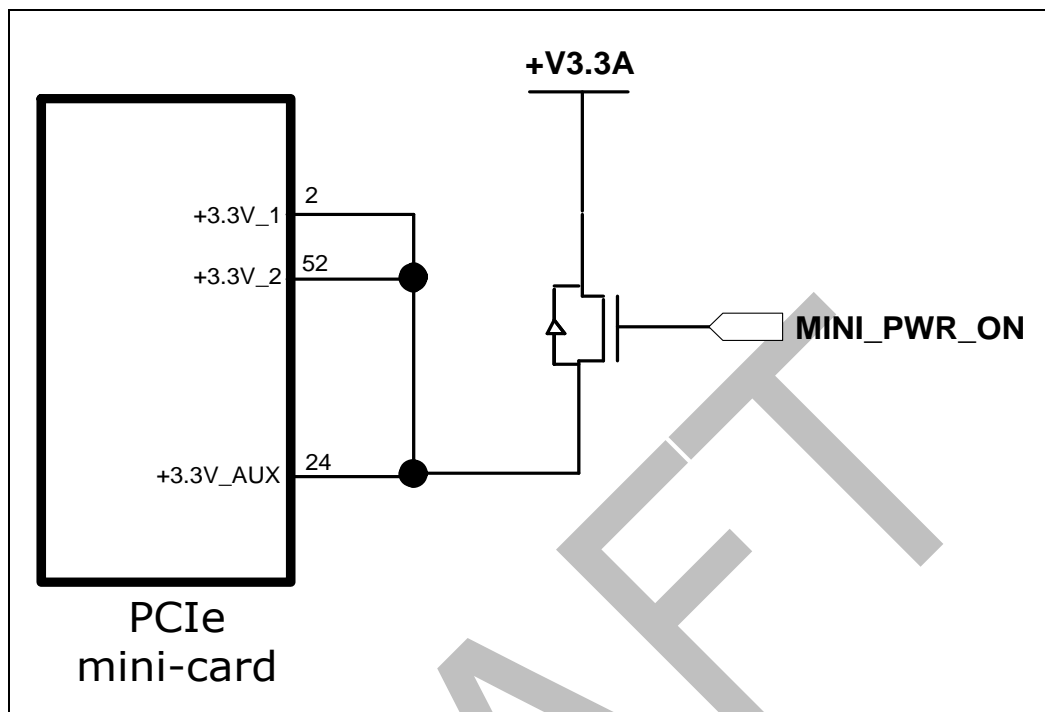
- Ability to be powered in S3/DeepSx (optionally S4 if Intel® Rapid Start Technology is supported) by OEM determined +3.3V power rail that is powered in AC and Battery modes.
 1. Voltage must be stable when transitioning to S3/DeepSx (S4 optionally)
 2. Power controlled by BIOS/EC regardless of Intel® ME configuration using on/off state specified by SWLS ACPI Control Method
- Connection of PCIe* WAKE# pin to PCH or EC for NetDetect platform wakeup when recognized Access Point is detected in S3/DeepSx.
- PCIe PERST# pin is asserted (low) in S3/DeepSx and Rapid Start S4. The WLAN card knows that NetDetect is active and ignores the reset request.
- The state of RF Kill must not change when the platform transitions from S3 to S0. If the state changes and the platform was woke for NetDetect, the uCode of the WLAN card will not indicate the PME wake was for NetDetect.

For each Intel® Smart Connect Technology NIC supported (WLAN, the following hardware support is **optional**:

- Radio On/Off switch or equivalent hot key to disable the RF transmission on the platform. If prior to entering S3 and the radio is turned off, The Intel® Smart Connect Technology Agent software will not schedule a timer based wake up or enabled NetDetect.

Note: Since the PCH requires the Suspend Well powered in the Sx state for handle PCIe* wake events, an alternative is to use the EC to monitor PCIe* wake events and avoid powering the Suspend Well in S3 (or S4).

The following figure shows an example implementation of Intel® Smart Connect Technology with optional FET to control power to mini-card during S3.

Figure 3-1. Example PCIe* Mini-card Power with Optional S3 Power Control

For platforms implementing DeepSx, use +V3.3A_DS3 instead of +V3.3A. This figure only applies to EC controlled WLAN power in S3. Please refer to the series 9 chipset documentation for WLAN NIC power control

§

4 BIOS/EC

The Intel® Smart Connect Technology provides interfaces for the BIOS and Embedded Controller (EC) modify some of the runtime behavior of Intel® Smart Connect Technology. The BIOS is required to implement Intel® Smart Connect Technology ACPI methods that provide platform configuration information and runtime control required by the Intel® Smart Connection technology. The following sections provide details on those interfaces.

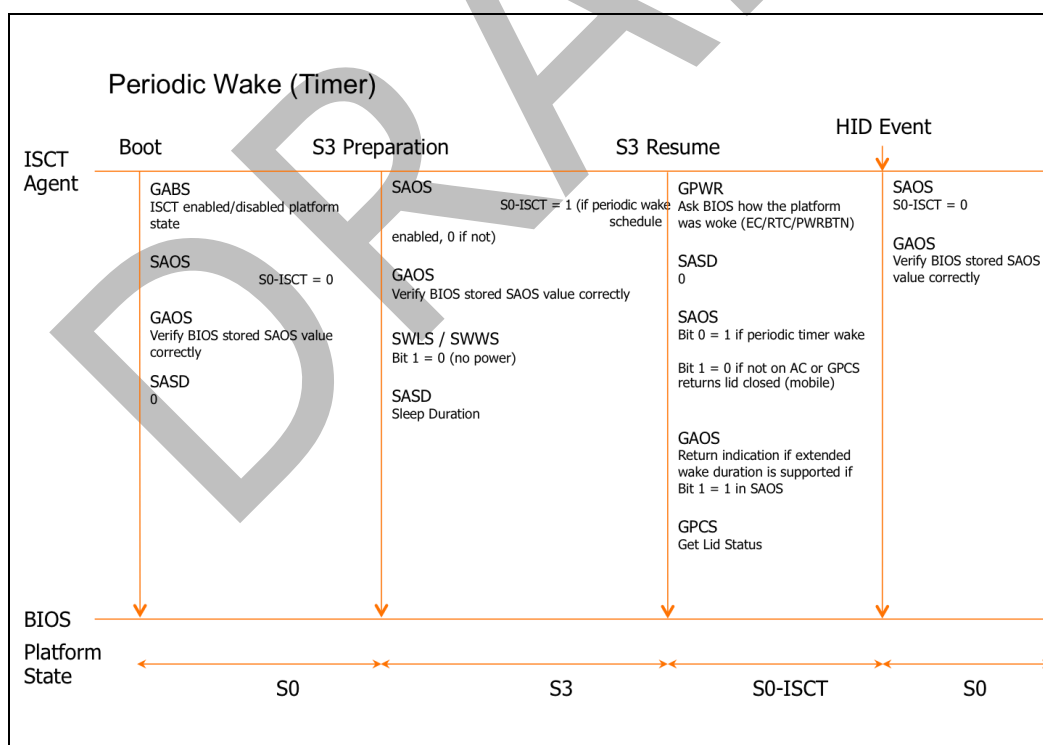
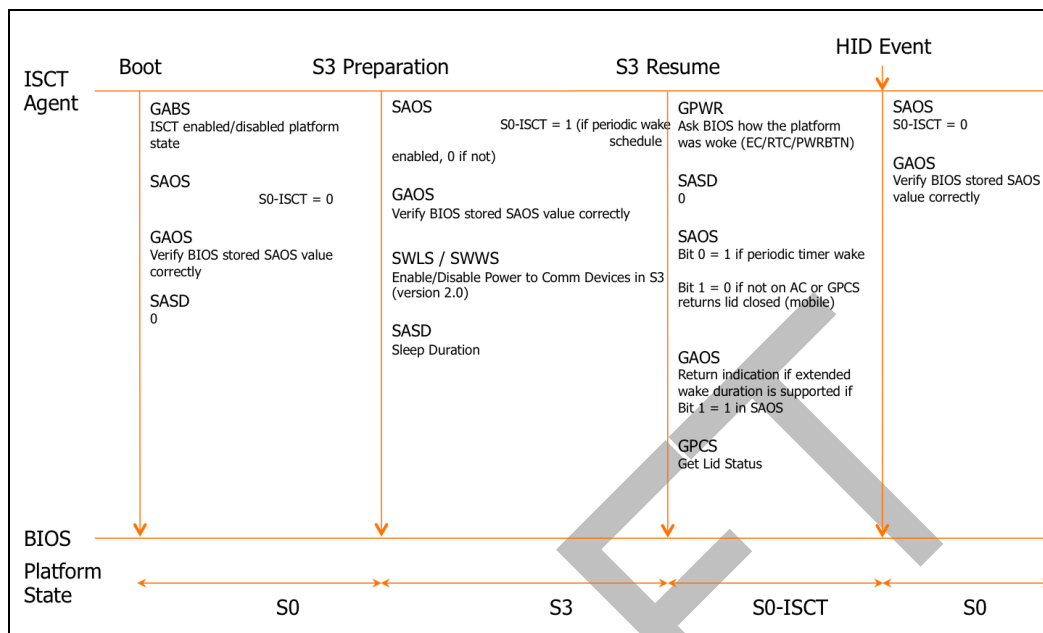
4.1 BIOS Requirements

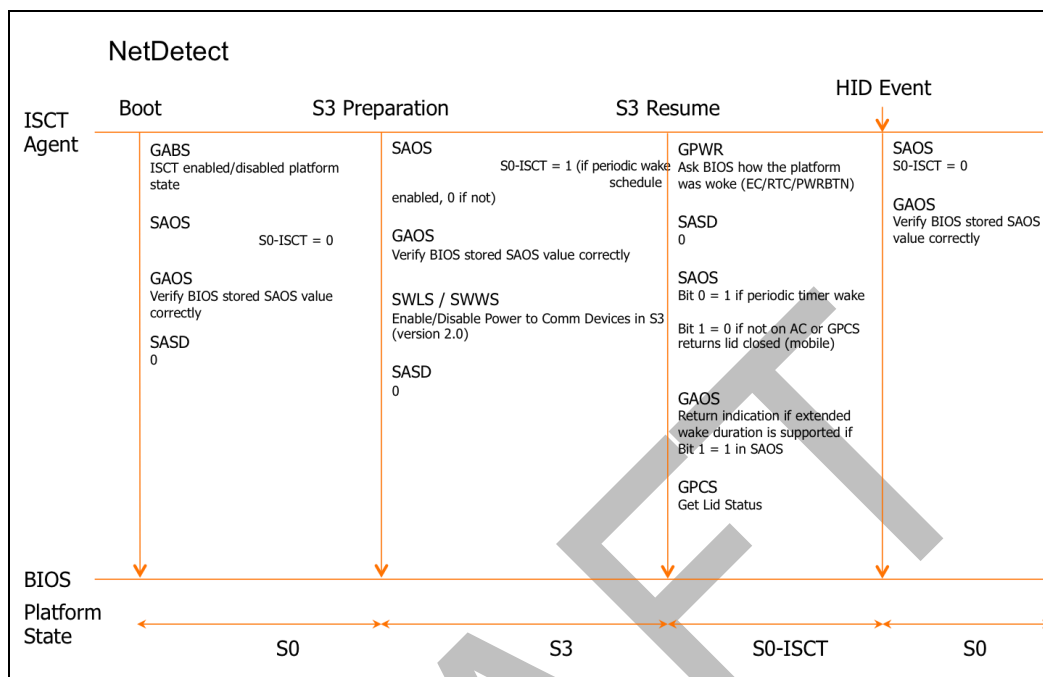
- Creation and support of the Intel® Smart Connect Technology ACPI pseudo device object with the ability to query/control via ACPI for the following Intel® Smart Connect Technology features:
 - Enable/Disable Intel® Smart Connect Technology mode
 - Indication of Intel® Smart Connect Technology is performing periodic wakes
 - Enable/Disable WLAN Module Powered in S3 (Rapid Start S4)
 - Platform Wake Reason (EC, Power Button, HID Event, or RTC)
- Ensure the PCI Express* Wake Disable (PCIEXP_WAKE_DIS) bit 14 in PM1_EN PCH register is set to 0b to allow wakes by the Intel WLAN card.
- The WAKE# Pin DeepSx Enable (WAKE_PIN_DSX_EN) bit (bit #2) must be set to '1' in the DeepSx Configuration (DSX_CFG) PCH configuration register for NetDetect to work from DeepSx if the platform supports DeepSx

Note: This is the default setting of the register. This is not a requirement for desktop platforms.

- Clear PCI Express* Wake Status (PCIEXPWAK_STS) bit 14 in PM1_STS PCH register upon wake to prevent “bounce” or wake happening again after platform goes back to S3.
- If a USB device is used to generate PME wake event, ensure the PME_EN bit 11 in the General Purpose Event 0 Enables Register (GPE0_EN) PCH register is set to 1b to allow USB device wakes.
- SMBIOS Type-24 Hardware Security Table if the BIOS implements passwords for either Power On or Administrative. The Intel® Smart Connect Technology Agent reads the fields and if either of these fields is set, then the Agent does not enable Periodic Wake from S4 or Remote Wake from S4 because BIOS during the unattended wakeup of the platform from S4 state would require a password from the user and no user may be present.

The following figures illustrate the interaction between the Intel® Smart Connect Technology Agent and the BIOS.


Figure 4-1. Intel® Smart Connect Technology Agent and BIOS Interaction




4.1.1 Programming the Periodic Wake Timer

The periodic wake timer can be either the RTC wake timer (for systems without EC) or EC timer (required for mobile platforms). Intel® Smart Connect Technology Agent will call the SASD ACPI control method with the timer value to program the timer to. For both mobile and desktop platforms BIOS has to program either the EC or RTC timer as described in the following sections. The periodic wake timer is only programmed in S3 and S4 by the BIOS or EC.

EC Timer is the preferred method of periodic timer capability because:

- No contentions with OS Programmed RTC timer events
-
- EC upon timer expiration can check platform sensor state (e.g. thermal, accelerometer, etc.) and prevent or delay wake if conditions not good

4.1.1.1 Housekeeping Wake-once-a-day Timer

- If no Periodic Wake event is scheduled in a 24-hour period (for example on weekends), a wake is scheduled once a day at the end of the Extended Power Savings time (AM) to ensure the RW infrastructure is working.
- If Periodic Wake is not enabled, then the Agent will set a wake event within 24 hours of the platform going to S3 / S4



- Agent uses the SASD ACPI control method to set the once a day wake time. If the EC timer cannot accept a value of 24 hours, then EC should use maximum timer value possible.

If the WLAN NIC is not able to send the Keep Alive packet due to network connectivity issues, a retry of 3 times occurs before the WLAN NIC wakes the platform momentarily to restart the RW infrastructure.

4.1.1.2 RTC Timer (Desktops without EC Timer Capability)

Because the RTC timer is a shared resource on the platform, prior to programming the RTC timer with the periodic wake value, the BIOS needs to check if an existing RTC wake setting is earlier than the requested periodic wake time. If the value is earlier, then the BIOS will not override the setting with the SASD value.

For RTC timer usage, it will be necessary to specify in the GABS ACPI control method that the SASD timer value is absolute (actual) format since the current RTC timer value will be in an absolute format (yyymmddhhmmss).

Ensure that the timer values stored in NVS are valid and properly initialized on boot (either the default Intel® Rapid Start Technology entry timer value or all 1's if only Smart Connect Technology). A wake timer value of zero implies immediate wake for the given timer; therefore, don't zero initialize a timer.

Upon receiving the SASD ACPI control method:

- Save the SASD timer value in ACPI NVS

Upon entry to S3:

- If Intel® Rapid Start Technology is enabled, then the following checks are performed between the saved SASD (iSCTTimer), current RTC timer (RTCValue) and Rapid Start timer (RapidStartTimer). With the appropriate timer value programmed in the RTC:
 - a. If (RTCValue <= iSCTTimer) AND (RTCValue <= RapidStartTimer) THEN No RTC overwrite required (do not program the RTC timer)
 - b. Else If (iSCTTimer <= RapidStartTimer) THEN Program RTC = iSCTTimer
 - c. Else Program RTC = RapidStartTimer while preserving the difference (new timer value) between the existing FFStimer value and iSCTTimer. This preserved new timer value will be programmed during S3
→RapidStart(S4) entry
- If Intel® Rapid Start Technology is *not* enabled or used, then the following checks are performed between the SASD (iSCTTimer) and current RTC timer (RTCValue). With the appropriate timer value programmed in the RTC:
 - a. If (RTCValue <= iSCTTimer) THEN No RTC overwrite required (do not program the RTC timer)
 - b. Else Program RTC = iSCTTimer



Upon RTC wake from S3:

1. BIOS checks the RTC timer value against the known iSCTTimer and RapidStartTimer values (previously preserved) to identify whether the wake is Smart Connect Technology or Rapid Start Technology related or something else (OS programmed wake event)
2. In the case of Smart Connect Technology, report in GPWR ACPI control method return value that the wake was for S0-ISCT. If BIOS determines at time of S0-ISCT RTC Wake that platform conditions are not appropriate for S0-ISCT mode, it can delay the OS resume and return back to S3
3. In the case if wake is Rapid Start Technology related wake event, BIOS will take the Intel® Rapid Start Technology entry flow
4. In the case if both timers are equal, BIOS gives priority to the ISCTTimer
5. If the RTC timer value is neither Intel® Smart Connect Technology or Intel® Rapid Start technology timer value than BIOS will take the normal S3 resume path and report in GPWR ACPI control method return value that the wake was for non-periodic RTC wake.

4.1.1.3 EC Timer

Specify in the GABS ACPI control method that the SASD timer value is "duration" format

Upon receiving the SASD ACPI control method:

- Save the SASD timer value in ACPI NVS

Upon entry to S3:

- Program the EC wake timer using the value saved for the SASD value. It is acceptable if the EC wake timer has a small drift in its timer resolution. The drift should not exceed 10%. If the value of SASD is too large for the EC wake timer, then use the maximum EC wake timer programmable value (e.g. 15 hours instead of 24 hours).

Upon wake from S3:

- Report in GPWR ACPI control method return value that the wake was for S0-ISCT.
- Turn off the backlight so that the OS does not turn on the display. OS treats EC wake as user event and turns on the display.

4.1.2 BIOS ACPI Requirements

4.1.2.1 IAOE _HID Object

The BIOS is required to create an ACPI pseudo device _HID object with a value of EISAID ("INT33A0") under ACPI device tree "_SB.IAOE". This identifies the ACPI object as an Intel® Smart Connect Technology ACPI pseudo device. The Intel® Smart Connect Technology SW requires this device to be present during installation of the SW.



4.1.2.2 IAOE _CID Object

It is recommended the BIOS create an ACPI pseudo device _CID object with a value of PNP0C02 under ACPI device tree “_SB.IAOE”. Typically OS will use _HID to locate the matching device driver. If no matching device driver is found, it will use _CID to locate the compatible device driver. Since PNP0C02 is pointed to Motherboard Resources, OS will install the default device drive for the device using the setup file machine.inf.

If the platform is re-imaged not using the original recovery disk, it could lead to an "Unknown device" for ISCT device in Device Manager. This could happen in IT deployment and any other time when the machine is reconfigured for other purpose.

4.1.2.3 IAOE ACPI GNVS Variables

The BIOS will need to allocate a small set of ACPI GNVS variables that will reflect the run time IAOE configuration settings (i.e., IAOE WLAN Setting, IAOE EC Timer setting value). A set of ACPI control methods (defined later in this section) will provide the capability to these variables at runtime through Intel® Smart Connect Technology OS driver.

4.1.2.4 IAOE ACPI ASL Control Methods

The BIOS must provide the following control methods to support Intel® Smart Connect Technology on the platform. These control methods are architectural and must be implemented in the BIOS ASL code under the IAOE ACPI Device Object (PnP ID INT33A0).

Table 4-1. IAOE Intel® Smart Connect Technology Control Methods

Control Method	Description
GABS	Intel® Smart Connect Technology BIOS Enabled Setting
GAOS	Intel® Smart Connect Technology Function Status
SAOS	Intel® Smart Connect Technology Function Status
GANS	Intel® Smart Connect Technology Notification Status
SANS	Intel® Smart Connect Technology Notification Status
GWLS	Get WLAN Module Status
SWLS	Set WLAN Module Status
GWWS	Get WWAN Module Status
SWWS	Set WWAN Module Status
GPWR	Get Platform Wake Reason
SASD	Set Intel® Smart Connect Sleep Duration
GPCS	Get Platform Component State
GAWD	Intel® Smart Connect Technology Maximum Wake Duration
SAWD	Intel® Smart Connect Technology Maximum Wake Duration



4.1.2.4.1 IAOE ACPI Control Method GABS

This control method returns the enabled/disabled status of Intel® Smart Connect Technology configured in the BIOS. The Intel® Smart Connect Technology Agent reads these values at OS boot time when the Agent begins execution. The values are set by the BIOS at boot time and not changed during S0. This allows the BIOS/EC to specify what Intel® Smart Connect Technology functions the Intel® Smart Connect Technology SW is allowed.

Arguments:

None – Control method takes no input arguments.

Table 4-2. IAOE Control Method GABS

Return Value (Bits)	Description
0	Intel® Smart Connect Technology Configured: 0 = Disabled, 1 = Enabled
1	Intel® Smart Connect Technology Notification Control: 0 = Unsupported, 1 = Supported
2	Intel® Smart Connect Technology WLAN Power Control: 0 = Unsupported, 1 = Supported
3	Intel® Smart Connect Technology WWAN Power Control: 0 = Unsupported, 1 = Supported
4	Must be set to 1
5	Sleep duration value format: 0 = Actual time, 1 = duration in seconds (see SASD for actual format)
6	RF Kill Support (Radio On/Off): 0 = Soft Switch, 1 = Physical Switch
7	Wake from OS S4: 0 = Unsupported, 1 = Supported

4.1.2.4.2 IAOE ACPI Control Method GAOS

The Get Intel Smart Connect Technology Function Status control method returns the current status of Intel® Smart Connect Technology S0-ISCT state in the BIOS as set by the SAOS method. Refer to the section on SAOS for the return value handling of Bit 1.

Arguments:

None – Control method takes no input arguments.

Table 4-3. IAOE Control Method GAOS

Return Value (Bits)	Description
0	Intel® Smart Connect Technology Mode: 0 = Disabled, 1 = Enabled
1	Wake Mode Select: 0 = ISCT Wake Mode, 1 = Extended ISCT Wake Mode



2	Wake from OS S4: 0 – No wake from S4, 1 – Wake from S4
3	Wake from DC to AC: 0 – No wake from DC to AC, 1 – Wake from DC to AC if wake criteria is met.
4-7	Reserved (set to 0)

4.1.2.4.3 IAOE ACPI Control Method SAOS

The Intel® Smart Connect Technology Agent calls the SAOS function and sets Intel® Smart Connect Technology Mode (bit 0) to 1 (S0-ISCT state) prior to suspending the platform when Periodic Wake is enabled by the Intel® Smart Connect Technology Configuration Utility (GUI). The value will remain 1 until a user event (power button, keyboard/mouse, lid switch) returns the OS to the non S0-ISCT state. This allows the BIOS/EC to know when SCT is performing idle time data updates and turn off unneeded LEDs, fans and other power savings features. Upon calling of the SAOS function by the Intel® Smart Connect Technology Agent, BIOS will store the value and return it via the GAOS function. The S0AS method is also called at boot time by the Agent to notify the BIOS of the current setting of the S0-ISCT state (the value will be 0 at boot time).

Bit 1 is set when Bit 0 (ISCT Mode) is set to 1 (enabled). Bit 1 indicates to the BIOS whether the wake duration will be extended (until network activity subsides) or normal (based on email traffic). If the BIOS can support extended wake duration, then BIOS will return a 1 for bit 1 in the GAOS ACPI control method. If it cannot, then BIOS will return a 0 for bit 1 in the GAOS ACPI control method. Bit 1 is set to 0 if the platform is not on AC (detected by Agent).

The following table illustrates the value of Bit 1 for the various conditions.

Table 4-4. SAOS Bit 1 Value

Bit 1 Value	AC Status
0	On Battery
1	On AC

Bit 2 is set to 1 when the Agent determines that a wake from OS S4 is requested of the BIOS. If the BIOS *does not* want the wake to occur, it indicates this to the Agent by setting Bit 2 to 0 in the return GAOS call. If allowed, set Bit 2 to 1 in the return GAOS call.

Note: The SAOS method is only called if the BIOS have set Bit 0 of the GABS method to 1b.

The Agent calls the SAOS ACPI control method before the OS sends PBT_APMSUSPEND when the system is in S0-ISCT. The Agent calls SAOS after PBT_APMSUSPEND if the platform was in normal S0 and the platform transitions to S3 by user or unattended timer expiration.

Arguments:



Arg1: **Intel® Smart Connect Technology Function Index** (Integer), where each bit in the Intel® Smart Connect Technology Function Index corresponds to the enable/disable status of an Intel® Smart Connect Technology function or a sub-function.

Table 4-5. IAOE Control Method SAOS

Intel® Smart Connect Technology Function Index (Bits)	Description
0	Intel® Smart Connect Technology Mode: 0 = Disabled, 1 = Enabled
1	Wake Mode Select: 0 = ISCT Wake Mode, 1 = Extended ISCT Wake Mode <ul style="list-style-type: none">ISCT Wake Mode: Wake Mode: BIOS or EC should not turn on the fan initially, leave the display backlight off, keep all system LEDs and power indicators in the S3 state. BIOS or EC should also enable the platform watchdog timer if it is used.Extended ISCT Wake Mode: BIOS or EC should not turn on the fan initially, leave the display backlight off, keep all system LEDs and power indicators in the S3 state. BIOS should also disable any wake duration watchdog timer if used.
2	Wake from OS S4: 0 – No wake from S4, 1 – Wake from S4
3-7	Reserved

4.1.2.4.4 IAOE ACPI Control Method GANS

The Get Intel® Smart Connect Technology Notification Status control method returns the current enabled/disabled status of Intel® Smart Connect Technology notification in the BIOS as set by the SANS method.

Arguments:

None – Control method takes no input arguments.

Table 4-6. IAOE Control Method GANS

Return Value (Bits)	Description
0	Intel® Smart Connect Technology Notification: 0 = Disabled, 1 = Enabled
1-7	Reserved

4.1.2.4.5 IAOE ACPI Control Method SANS

The Intel® Smart Connect Technology Agent will call the SANS method and set the bit to '1' when an application indicates that new data is available for the user. Upon the user returning the platform to normal S0 mode, the Intel® Smart Connect Technology Agent will set this bit to 0. Upon calling of the SANS function by the Intel® Smart Connect Technology Agent, BIOS stores the value and returns it via the GANS function.

**Arguments:**

Arg1: **Intel® Smart Connect Technology Notification Index** (Integer), where each bit in the Intel® Smart Connect Technology Notification Index corresponds to the enable/disable status of an Intel® Smart Connect Technology notification.

Table 4-7. IAOE Control Method SANS

Intel® Smart Connect Technology Notification Index (Bits)	Description
0	Intel® Smart Connect Technology Notification: 0 = Disabled, 1 = Enabled
1-7	Reserved

4.1.2.4.6 IAOE ACPI Control Method GWLS

The Get WLAN Module Status control method returns the current WLAN module HW state of enabled/disabled in the BIOS.

Arguments:

None – Control method takes no input arguments.

Table 4-8. IAOE Control Method GWLS

Return Value (Bits)	Description
0	Reserved (set to 0)
1	WLAN Module Powered in S3:0 = Disabled, 1 = Enabled
2	WLAN Module Powered in S4 (OS S4):0 = Disabled, 1 = Enabled
3	WLAN Module Powered in S5:0 = Disabled, 1 = Enabled
4-7	Reserved (set to 0)

4.1.2.4.7 IAOE ACPI Control Method SWLS

The Set WLAN Module Status control method is called by the Intel® Smart Connect Technology Agent to instruct the BIOS/EC of when to enable/disable power to the WLAN.

Arguments:

Arg1: **WLAN Powered Index** (Integer), where each bit in the Intel® Smart Connect Technology WLAN Powered Index corresponds to the enable/disable state.



Table 4-9. IAOE Control Method SWLS

WLAN Powered Index (Bits)	Description
0	N/A (WLAN Wireless Disable is Read only) – Always set to 0
1	WLAN Module Powered in S3:0 = Disabled, 1 = Enabled
2	WLAN Module Powered in S4 (OS S4):0 = Disabled, 1 = Enabled
3	WLAN Module Powered in S5:0 = Disabled, 1 = Enabled
4-7	Reserved

4.1.2.4.8 IAOE ACPI Control Method GWWS

The Get WWAN Module Status control method returns the current WWAN module HW state of enabled/disabled in the BIOS and whether the WWAN module is to support L2 connectivity.

Arguments:

None – Control method takes no input arguments.

Table 4-10. IAOE Control Method GWWS

Return Value (Bits)	Description
0	Reserved (set to 0)
1	WWAN Module Powered in S3:0 = Disabled, 1 = Enabled
2	WWAN Module Powered in S4 (OS S4):0 = Disabled, 1 = Enabled
3	WWAN Module Powered in S5:0 = Disabled, 1 = Enabled
4-7	Reserved (set to 0)

4.1.2.4.9 IAOE ACPI Control Method SWWS

This Set WWAN Module Status control method is called by the Intel® Smart Connect Technology Agent to instruct the BIOS/EC of when to enable/disable power to the WWAN. BIOS upon per executing the request will update the GWWS return values.

Arguments:

Arg1: **WWAN Powered Index** (Integer), where each bit in the Intel® Smart Connect Technology WWAN Powered Index corresponds to the enable/disable state.

Table 4-11. IAOE Control Method SWWS

WWAN Powered Index (Bits)	Description
0	N/A (WWAN Wireless Disable is Read only) – Always set to 0



WWAN Powered Index (Bits)	Description
1	WWAN Module Powered in S3:0 = Disabled, 1 = Enabled
2	WWAN Module Powered in S4 (OS S4):0 = Disabled, 1 = Enabled
3	WWAN Module Powered in S5:0 = Disabled, 1 = Enabled
4-7	Reserved

4.1.2.4.10 IAOE ACPI Control Method GPWR

The Intel® Smart Connect Technology Agent calls the GPWR method to get the wake reason from the BIOS after resume from S3 or S4. The Agent determines whether to remain in the S0-ISCT mode using the return values from the BIOS. For example, if Bit 0 is set to '1' and the rest of the bits set to '0', this indicates to the Agent that the S3 or S4 Sleep state was interrupted by the user and that the Agent should not perform periodic update and exit to the S0 state and will call SAOS ACPI method with a value of '0' to indicate to the BIOS/EC that periodic wake/sleep mode has been halted.

If the wake was for Periodic wake by EC Timer or RTC Timer, then Bit 0 is set to '0' and set Bit 1 to '1'. If the EC or RTC Wake timer was not for the S0-ISCT mode (for example user set RTC timer), then set Bit 0 to '0', Bit 1 to '0' and set Bit 2 to '1'.

If the wake is for PME Wake, BIOS enumerates the PCI devices to identify if the waking device is the WLAN card. If the WLAN card did the PME wake, then set Bit 3 to '1'. If any other device caused the PME wake, then set Bit 3 to '0' and set Bit 0 to '1'.

If the platform supports the Intel® ME and the wake was caused by the Intel® ME, then set Bit 5 to '1'.

Arguments:

None – Control method takes no input arguments.

Table 4-12. IAOE Control Method GPWR

Intel® Smart Connect Technology Function Index (Bits)	Description
0	User pressed power button or HID event
1	Periodic Wake BIOS timer caused wake (EC or RTC)
2	Non-Periodic Wake RTC timer caused wake
3	Wake due to PME and a network device. BIOS enumerates PCI devices to identify the waking device. If the waking device is a network device and PCH wake reason is PME, BIOS sets this bit. Otherwise, BIOS sets bit 0.
4	Reserved (set to 0)
5	Wake due to Intel® ME. BIOS sets this bit if Intel ME causes wake, else set to 0.
6-7	Reserved (set to 0)



4.1.2.4.11 IAOE ACPI Control Method SASD

The Intel® Smart Connect Technology Agent calls the SASD method with the number of seconds to program the platform timer (EC or RTC) to for the next periodic wake. Upon resume from S3 or initial boot of platform, the Agent will call the SASD method with a value of 0 to indicate there is no wake scheduled until called prior to S3 entry. Because the Agent cannot discern between S3 and OS Hibernate S4, the SASD method will be called in both sleep states. It is the BIOS responsibility to detect which sleep state the platform is transitioning to and only program the timer upon S3 transition.

If a RTC timer is used, the Intel® Smart Connect Technology Agent always treats RTC wake as an Intel® Smart Connect Technology wake even if the RTC wake was scheduled by another application because the RTC wake cannot be associated with who scheduled it. To prevent the Agent from handling all RTC wake events, the BIOS can compare the time passed by SASD and RTC value after resume from S3 or S4. If the two values match, the BIOS informs the Agent by returning Bit 2 = '1' (RTC timer) when the GPWR ACPI control method is called by the Agent upon resume. Otherwise if the two values do not match, BIOS returns Bit 0 = '1' and the rest of the bits as zero of the GPWR ACPI control method. This informs the Agent to not enter the S0-ISCT mode and keep the system in S0 mode so that the non-ISCT RTC wake will be handled by the application that scheduled it.

If the ACPI Control Method GABS indicates an actual time is to be used, the following encoding is used:

Bit 31: 0 = Sleep Duration in seconds for Bits 0 thru 30

Bit 31: 1 = Actual time with Bits 0 thru defined as:

Bits 26-30: Year offset from 2000 (e.g.11 denotes 2011)

Bits 22-25: Month

Bits 17-21: Day

Bits 12-16: Hour (24 hour format)

Bits 6-11: Minutes

Bits 0-5: Seconds

If the ACPI Control Method GABS indicates duration is to be used, the BIOS/EC needs to determine if the sleep duration in seconds is too large for the timer resolution before programming the timer. If the value is too large, then use the largest value programmable.

If NetDetect is enabled, the SASD value for all bits will be 0 indicating no periodic wake is required and the BIOS is not required to set a timer wake value.

Arguments:

Arg1: **Intel® Smart Connect Technology Sleep Duration** where the value is the Intel® Smart Connect Technology Agent Sleep Duration in seconds.

**Table 4-13. IAOE Control Method SASD**

Intel® Smart Connect Technology Function Index (Bits)	Description
0-31	Sleep Duration value in seconds (e.g. 900)

4.1.2.4.12 IAOE ACPI Control Method GPCS

The Intel® Smart Connect Technology Agent calls the GPCS method to get the current state of platform components.

Arguments:

None – Control method takes no input arguments.

Table 4-14. IAOE Control Method GPCS

Intel® Smart Connect Technology Function Index (Bits)	Description
0	Lid state: 0 – Closed, 1 – Open (refer _LID in section 9.4 of the ACPI 5.0 specification). Note: If the platform does not have a lid (e.g. desktop or tablet), then return '1' to indicate open Note: If the system is in tablet mode, then return '1' to indicate open
1	Support Wake from Power Source Change: 0 – Don't support wake from power source change, 1 – BIOS/EC supports wake from power source change.
2-7	Reserved (set to 0)

4.1.2.4.13 IAOE ACPI Control Method GAWD

The Get Intel Smart Connect Technology Maximum Wake Duration control method returns the current S0-ISCT wake duration value in the BIOS as set by the SAWD method.

Arguments:

None – Control method takes no input arguments.

Table 4-15. IAOE Control Method GAWD

Return Value (Bits)	Description
0 – 31	Intel® Smart Connect Technology Maximum Wake duration in S0-ISCT mode



4.1.2.4.14 IAOE ACPI Control Method SAWD

The Intel Smart Connect Technology Agent will call the SAWD method to set the maximum S0 duration that the platform will be in S0-ISCT mode. This time does not include the BIOS resume time, OS resume time and OS suspend time. Due to OS thread scheduling reasons, the actual maximum time may be slightly more than the below threshold. EC can use this value to program its own watchdog timer in case the S0-ISCT session experiences an application that refuses to allow the OS to transition to S3 at the end of the wake session. Upon calling of the SAWD function by the Intel Smart Connect Technology Agent, BIOS stores the value and returns it via the GAWD function.

Arguments:

Arg1: **Intel Smart Connect Technology Notification Index** (Integer), value of the S0-ISCT maximum wake duration in seconds

Table 4-16. IAOE Control Method SAWD

Intel® Smart Connect Technology Notification Index (Bits)	Description
0 – 31	Intel® Smart Connect Technology Maximum Wake duration in S0-ISCT mode

4.1.2.5 IAOE Control Method Values Summary

The following table summarizes the values passed from the Intel® Smart Connect Technology Agent to the BIOS during the transition to S3.

Table 4-17. IAOE Control Method Values Summary

Events	SAOS	SASD	SWLS	NetDetect	Notes
User disable ISCT via GUI	0	N/A	N/A	Disabled	
User Enable ISCT via GUI, Radio On	1	If network connectivity, non-zero timer value	If no network connectivity in S0 (LAN/WLAN), SWLS=2, else 0	Enabled if no network connectivity, disabled if network connectivity	
User Enable ISCT via GUI, Radio Off	1	0	See below as it depends on type of Radio On/Off switch	Enabled if no network connectivity, disabled if network connectivity	Timer is disabled if Radio is off. Required for FAA compliance. Once user exits aircraft, radio can be turned back on to allow NetDetect wake



4.2 Display Handling

Periodic Wake (timer wake) is handled by the Operating System as a user event and the display will be turned on upon the transition from S3 to S0-ISCT by the Operating System. RTC wake, PME wake and SCI_EN wake events are treated by the Operating system as an unattended wake and the Operating System does not turn on the display. Additionally (although rare), an application running during the S0-ISCT state may turn the display on.

To prevent the display from turning on as the result of a Periodic Wake the EC or BIOS is required to turn off the display upon wake. The method to perform this depends on the display type and the Graphics adapter on the platform

4.2.1 Non-Intel® HD Graphics

The EC can turn off the backlight of an LVDS panel when the ACPI method SAOS is called with a '1' (GAOS returns 1) and remain turned off until SAOS is called with a value of 0 (GAOS returns 0).

4.2.2 Intel® HD Graphics

The BIOS can instruct The Intel® HD Graphics driver to turn off the display (both internal and external) as follows:

Wake Reason	Display Control
Periodic timer wake	Turn off the display
PME Wake	If the Class Code indicates a network device, turn off the display
Intel® ME Wake	If the Class Code indicates a network device, turn off the display
All others	Leave display on

On normal boot (S4/S5) set offset 0x0060 (PCON) in the OpRegion Header to indicate the Intel® Smart Connect Technology capabilities of the platform as follows:

Bit Range[31:0]	Details
Bit [5]	ISCT Support Field 0 = Platform is not ISCT capable 1 = Platform is ISCT capable
Bit [6]	0 – Platform ISCT Capability Field is Not Valid 1 – Platform ISCT Capability Field is Valid

On S3 Resume, perform the following:



1. Verify the Graphics Device supports display disable notification for ISCT by checking if Bit 8 = '1' in TCHE (_SB.PCI0.GFX0.TCHE) register (offset 0x0308) of the Graphics Device. If it does not, then support is not available in the version of the Graphics driver installed on the platform.

Bit Range[31:0]	Details
Bit [8]	Graphics Driver support for ISCT 0 = No support for ISCT in Driver 1 = Support for ISCT in Driver. Driver would follow the ISCT path only when BIOS also reports support

2. Upon wake set the STAT (_SB.PCI0.GFX0.STAT) register (offset 0x03B6) of the Graphics Device with Bits 0/1 to '01' and not changing the other bits.

Bit Range[31:0]	Details
Bit [1:0]	00 – Normal Resume to S0 01 – ISCT Resume (Periodic Timer wake)
Bit [2-31]	Reserved

During S0-ISCT, if the user transitions the platform to S0 (e.g. HID Event or wake button press), when the Intel® Smart Connect Technology Agent calls the ACPI method SAOS with a '0' (return to S0), then the Graphics Device needs to be notified to turn on the display as follows:

1. Verify the Graphics Device supports display disable notification for ISCT by checking if Bit 8 = '1' in TCHE (_SB.PCI0.GFX0.TCHE) register (offset 0x0308) of the Graphics Device. If it does not, then support is not available in the version of the Graphics driver installed on the platform.

Bit Range[31:0]	Details
Bit [8]	Graphics Driver support for ISCT 0 = No support for ISCT in Driver 1 = Support for ISCT in Driver. Driver would follow the ISCT path only when BIOS also reports support

2. Set the STAT (_SB.PCI0.GFX0.STAT) register (offset 0x03B6) of the Graphics Device with Bits 0/1 to '0' and not changing the other bits.



Bit Range[31:0]	Details
Bit [1:0]	00 – Resume to S0 01 – ISCT Resume (Periodic Timer wake)
Bit [2-31]	Reserved

3. Notify the Graphics Device of the change by setting Bit 8 = '1' in the ASLC (_SB.PCI0.GFX0.ASLC) register (offset 0x0304).

Bit Range[31:0]	Details
Bit [8]	ISCT State change Indicator Requests Graphics to Turn on Display Devices as System has to be moved to S0 mode from current S0-ISCT mode 0 = No Request 1 = ISCT State Change Request

4. Generate ASLE interrupt by storing a value of '1' in the ASLE (_SB.PCI0.GFX0.ASLE) register (offset 0xE4).

4.3 Touch Panel

If the platform has a touch panel (either USB or I2C), BIOS/EC will disable the touch controller associated with the touch panel when the platform is in S0-ISCT as the result of ISCT managed wake event (EC timer, NetDetect, and Remote Wake) and enable the touch panel as the result of user action. The Intel® Smart Connect Technology Agent filter drivers for HID events cannot be used to handle the touch event as the Operating System does not allow touch panel events to be intercepted by a HID filter driver. Thus the Agent does not receive notification of the touch event and does not exit S0-ISCT to S0 and notify the BIOS/EC to return to S0 mode from S0-ISCT.

To allow the Agent to work correctly on platforms with a touch panel, the following is required:

- If the resume from S3 is for Periodic Wake Timer or PME wake (NetDetect or Remote Wake), disable the touch controller.
- If the resume from S3 is for user action (power button and keyboard/mouse event), BIOS enables the touch controller. This is to reduce the responsiveness impact.
- During S0-ISCT, if the user transitions the platform to S0 (e.g. HID Event or wake button press), when the Intel® Smart Connect Technology Agent calls the ACPI method SAOS with a '0', enable the touch controller.



4.4 Embedded Controller (EC)

4.4.1 Thermal

The EC should provide platform support for efficient and safe Intel® Smart Connect Technology operation. The EC requirements can be found in the next sections.

Intel® Smart Connect Technology usages, introduces new mobile system cooling challenges. Under normal Intel® Smart Connect Technology operation, systems are expected to update while they are unattended in environments or conditions that are not thermally desired, including in a bag, in trunk, etc. Layers of thermal protection must be in place to protect against all scenarios including case when OS lost control of execution. For example, the EC can check the thermal sensors and determine that the platform is in an environment that is too hot and subsequently direct the system to not wake up to S0, but instead reprogram the wake EC timer for a later time and remain in S3. This also can be done for checking the battery levels to force remaining in S3. The EC must also support the ability to turn off the platform if the platform exceeds the OEM defined thermal levels in which any of the platform components can be damaged.

4.4.2 System Fans

Upon resume from S3 for the Intel® Smart Connect Technology S0-ISCT session, our requirement is that the systems fans remain turned off initially so that the platform appears as it was in S3. The Intel Smart Connect Technology Agent monitors the CPU temperature once every second during S0-ISCT session; upon reaching the thermal threshold (DeltaTjMax registry setting) the Intel Smart Connect Technology Agent requests the OS to transition back to S3.

When "Extended Wake Duration" is enabled by the BIOS via the GAOS ACPI Control Method (platform is on AC), the CPU temperature may start to rise steadily during the extended wake period. To prolong the S0-iSCT session when CPU temperature begins rising, the EC may spin the processor fan at a low RPM so that there is no acoustics event noticeable by the user. Running the fan will provide additional processor and system cooling to deliver desirable longer extended wake periods for certain applications such as large media downloads.

For Normal Wake Duration, the amount of time spent in S0-ISCT (maximum of 165 seconds) has been shown to not require the system fans to be turned on.

Fans should only be turned on if the following conditions are met:

1. System is running on AC
2. Lid is open for mobile systems
3. System is docked (lid state open or closed)

Note: We do not recommend running the fans when the system is operating with the lid closed and connected to AC (not docked) because of possible system in a bag scenario where the cooling of the system would be severely limited and possible pulling in of dust/lint that may be in the bag.



4.4.3 Power Button Override

Intel® Smart Connect Technology does not require any special processing for power button overrides. The EC should perform the normal operation of shutting down the platform if the power button is not released before the power button override time has elapsed.

4.4.4 Audio Muting

The Intel® Smart Connect Technology Agent will mute the audio (if not muted already) upon entering the S0-ISCT state and restore the audio mute setting upon exit to normal S0. To accomplish this and prevent race conditions, the Agent monitors when the Operating System or drivers set the mute setting and then mutes the audio afterwards. If an additional delay period needs to be set, add a DWORD registry entry "AudioDelayMilliseconds" and set the requested value in milliseconds (e.g. 3000) appropriately for the audio devices. See section 0 for more information on modifying registry values.

If the BIOS performs any audio notification upon transition from S3 to S0-ISCT, it is requested the BIOS does not do this to prevent the user from thinking the platform has been woken up. Upon resume from S3, the BIOS can check the state of GAOS to determine if the resume is to normal S0 or S0-ISCT.

4.4.5 Intel® Smart Connect Technology Mode Indicator LED

The platform can use the existing system status LED in two alternate modes when the user on the platform enables Intel® Smart Connect Technology:

1. Keep the system status LED indicating S3 state during S0-ISCT state (ACPI method SAOS is called with a value of 1 - GAOS returns 1) and return to normal S0 state indication when SAOS is called with a value of 0 (GAOS returns 0).
2. Provide notification to the user Intel® Smart Connect Technology is enabled in S3 (ACPI method SAOS is called with a value of 1), the system status LED shall turn on for 2 seconds every 20 seconds or indicate S0-ISCT mode for the brief period when S0-ISCT state is active (turn on 2 seconds every 20 seconds).

4.5 S3 State Considerations

Overview: When Intel® Smart Connect Technology is active and enabled on a system, and the user initiates an S3 transition, Intel® Smart Connect Technology Agent prepares the system for automatic wakeup after specific time duration to perform actions based on the current system conditions and configuration.

4.5.1 Power Button in S3 State

When the platform is in the S3 state and the power button is pressed, the behavior is to resume to the S0 state instead of the S0-ISCT state. By the user pressing the power button, they are indicating that they wish to have control of the platform. The Intel® Smart Connect Technology Agent needs notification from the EC that the power button was pressed to not enter the S0-ISCT state upon resume from S3. The BIOS



must set bit 0 of the ACPI control method GPWR that the power button was pressed to inform the SW Service to not enter the S0-ISCT state.

4.5.2 Intel® Smart Connect Technology Wake Timer Cycle Overview

Prior to suspending the platform, the Intel® Smart Connect Technology Agent will call the ACPI Control Method SASD with the value of the periodic wake. If this value is non-zero, the BIOS will instruct the EC to set a timer to wake the platform after a sleep of the specified seconds in the SASD value. When the timer expires the EC will wake the platform from S3 and instruct the BIOS to return a value of 1 in Bit 1 of the GPWR ACPI Control Method.

4.5.3 Platform Firmware Considerations for SAOS and SASD

When Intel® Smart Connect Technology is active and enabled, upon the user initiated S3 entry,

- Intel® Smart Connect Technology Agent evaluates the SAOS ACPI object with a parameter value of '1' to indicate the subsequent S0 could be an S0-ISCT mode.
- After programming the next wake timer (RTC or EC), the Agent evaluates the SASD ACPI object with a parameter value of "equal to the next wake timer" to be used by EC/BIOS for pre-SCT wake system check.

In response to SAOS object evaluation, the platform could prepare for next S0-ISCT mode. This includes the following:

- Set a flag within the firmware (BIOS and EC) to indicate while resuming that the system might be entering S0-ISCT mode.
- EC may use the next wake timer info to prepare from the upcoming wake.

4.5.4 Incomplete S3 Transitions

With Intel® Smart Connect Technology initiating more frequent S3 transitions than a typical system, there may be an increased vulnerability for an incomplete S3 transition and OS hanging in the middle. This could cause the system to remain active, causing battery drain and causing the system thermal issues.

It is recommended that the platform take additional precautions and monitor for such incomplete S3 transitions.

One possible solution to prevent such incomplete transitions is for EC to monitor System S3 transition completion (assertion of SLP_S3# pin from PCH) from the time SAOS ACPI object is evaluated. If after a predetermined period of time after SAOS ACPI object is evaluated or from any S3 entry request to OS, the EC fails to see SLP_S3# pin asserted, EC may cause a critical system shutdown to prevent system thermal violations.

The pseudo code illustrated in Example 1 below explains the algorithm that could be used in system EC.

Example 1. OS Hangs during S3 Entry Handler Pseudo Code

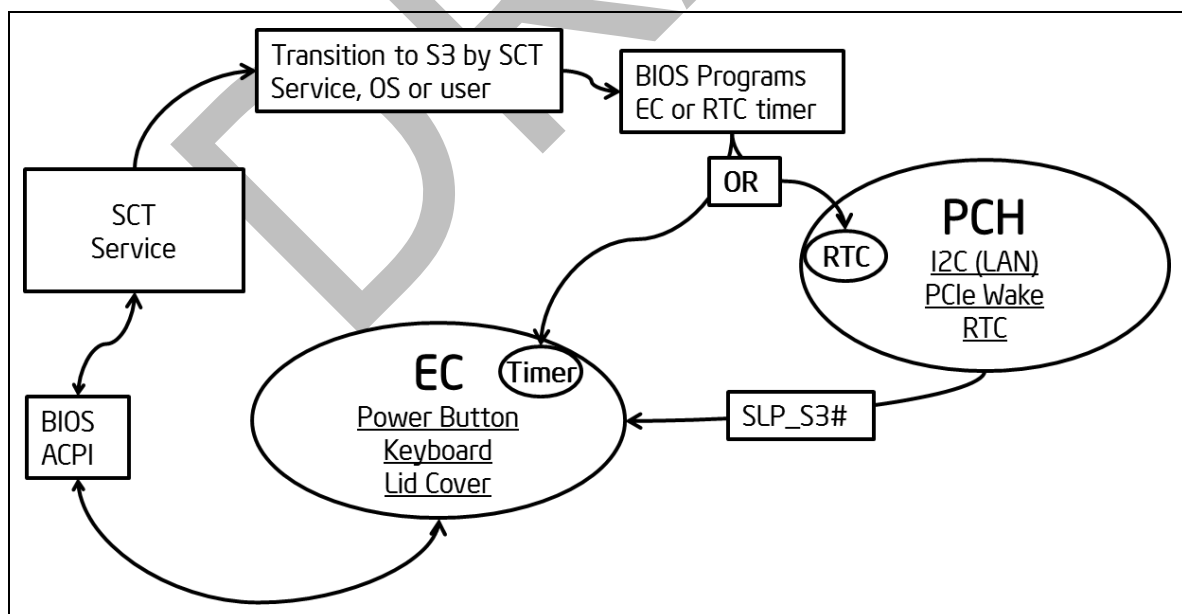
```

STEP 1: EC: Record time when SAOS or SASD Object is evaluated by the SCT
SW
STEP 2: EC: Wait Pre-determined time period (e.g. 30 to 60 seconds)
STEP 3: EC: Check if SLP_S3# is asserted to indicate system has entered
S3 state.
STEP 4a: If SLP_S3# is asserted, do nothing and exit
STEP 4b: If SLP_S3#
    • Is not asserted, cause a hardware critical thermal shutdown of the
      platform. Data loss may happen.
    • Program a wake in EC to check again
  
```

4.6 S0 State Considerations

Platform wake trigger can be initiated by many sources. The figure below conceptually illustrates the various platform triggers that could cause an S0 transition from S3. As seen from the illustration, there are certain events that the EC will be able to identify as a user event, while there are other events that the BIOS can read from PCH registers. The BIOS can determine the wake cause when it is handling the event from the PCH and/or EC and identify if the event is user triggered or not. All other triggers could be caused automatically without a user trigger. If Intel® Smart Connect Technology Agent is enabled it performs a quick check to determine if the wake event was an automatic wake or caused by a user activity. Any automatic wake is considered an SCT wake, SCT will take over and do the automatic updates and put the system back to Sx state per configuration.

Figure 4-2. System S0 State Transition Triggers





services and applications. The change to encounter this issue is very limited due to the short time duration.

4.6.2 How to Determine When the System is in S0-ISCT versus S0-Normal Mode

The GAOS ACPI control method returns the current status of the Intel® Smart Connect Technology Agent in the BIOS. Table below shows the bit value of GAOS during various system states.

EC and BIOS can use the information from SLP_S3# and GAOS to determine if the system is in S0-ISCT versus S0-Normal mode and can set the platform appropriately for automatic non-use operation.

SAOS values in various modes:

Table 4-18. SAOS Values

	S3	S0-ISCT	S0-Normal
SAOS	1	1	0
SLP_S3#	Asserted	De-asserted	De-asserted

4.6.3 S0-ISCT System State Design Considerations

The S0 system state triggered by Intel® Smart Connect Technology has some special considerations. Since the ISCT Agent performs its periodic tasks at times when the user may not be present in front of the system, the following special considerations may be applied when the system resumes in the S0-ISCT mode.

- **System Audio:** The platform may keep the system Audio turned off during S0-ISCT session. SCT Agent requests the OS to mute audio.
- **System Display and Backlight:** Until the platform knows for sure the current S0 entry is a user-triggered state, the platform may keep the system display and backlight turned off. SCT Agent requests the OS to keep the display off.
- **LED Indicators:** The platform may also keep the system status LEDs to indicate S3 status or indicate S0-ISCT mode indicator for the brief period when S0-ISCT state is active. If the system's power button indicates the system state, it should maintain the S3 indication state.
- **Thermal Trip Points (Mobile):** Under normal usage of S3 -> to S0-ISCT systems thermal are expected to be within acceptable range. SCT Agent checks for CPU temperature. A "backoff" mechanism is implemented in SCT Agent to start scheduling doubling the sleep duration time for system to cool sufficiently till next wake. Please refer to section [Chapter 7](#) for more details. Platform may use aggressive thermal trip points and manage the platform to those trip points during S0-ISCT session. This may include the platform notifying the Operating System Thermal Management Subsystem with new _PSV, _HOT, _CRT trip points. If EC is managing the system thermals, it is recommended that the EC use aggressive platform thermal limits.



- **System Fan(s):** Since the system operating environment is to be considered unknown when in S0-ISCT mode, the system fans are recommended to be turned off when in S0-ISCT mode. If desired, the fans may be turned on later in the S0-ISCT session if the thermals begin to rise and the system lid is open for mobile (to prevent operating environment where the notebook is in a bag). If the system is docked and lid is closed, it is acceptable to spin fans. This will allow longer Extended Wake Duration sessions where the system fans are turned off. For Normal Wake Duration there should be no need to turn on the system fans. If the fans are turned on, care should be taken to reduce the acoustics impact at the initial spin up so that fans spin up properly but not be ramped to full speed. Platform fan control is responsible for monitoring the system temperatures and fan speed management.

4.6.4 S0 Normal System State

This S0 system state indicates that the user has triggered a transition to S0 and hence the platform can safely assume normal usage modes and hence can apply normal thermal and platform settings.

- **System Audio:** System audio assuming user presence.
- **System Display and Backlight:** Display and backlight on assuming user presence.
- **LED Indicators:** Normal states as when the user is present. Additionally any specific SCT LEDs may be turned off by SCT service once SCT service determines the system is in S0 normal state.
- **Thermal Trip Points:** Trip points as applicable when a user is actively using the system may be applied.
- **System Fan(s):** System fans may be operated normally based on system thermal conditions.

4.6.5 S0-ISCT State Thermal Considerations

Similar to the concern described in section [4.5.4](#), any OS hangs that happen when in S0-ISCT mode with no user intervention could lead to system thermal violations. To prevent this from happening, it is recommended that the platform implement activity monitoring functionality to ensure the system is active and not hung.

The platform may implement a watchdog timer mechanism to notify the EC of system activity when in S0-ISCT mode. If the EC sees that the watchdog register/value does not get updated for several seconds, it can then trigger a forced shutdown assuming OS hang situations. For detailed discussion on platform thermal considerations refer to [Chapter 7](#).

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5 NetDetect

5.1 Overview

NetDetect is a feature of select WLAN devices. NetDetect improves the power consumption of Intel® Smart Connect Technology by replacing periodic wakes with network detection wakes. NetDetect enables the WLAN device to scan for Access Points (APs) while the system is in the S3 state. NetDetect is implemented in the microcode of the WLAN device.

5.2 Architecture

Prior to entering S3 and Intel® Smart Connect Technology is enabled for periodic wakes, the Intel® Smart Connect Technology Agent will determine network connectivity by querying the Microsoft* Windows Connection Manager*. If the request to enter S3 is during the S0-ISCT mode and no network connection is available, the SCT Agent will query the Intel® WLAN Driver to determine if the WLAN device supports NetDetect. If NetDetect is available, the SCT Agent will ask the WLAN driver to enable NetDetect. The SCT Agent will also call the BIOS's ACPI SWLS ACPI control method and ask the BIOS to enable power to the WLAN device during S3. If the request to enter S3 is from the user or result of unattended usage, NetDetect is not configured by the SCT Agent due to the short period of time (typically 2 seconds) that the SCT Agent has to transition to S3.

Once the platform is in S3, and an initial hold-off time equal to the current periodic wake sleep period occurs (30 minutes in extended hours), the microcode of the WLAN device will periodically (about every two minutes) turn on the receiver of the WLAN NIC to scan for APs. If an AP is found, the microcode will compare the SSID to any APs that were previously configured by the user as "**Connect automatically**".

If the SSID matches, the microcode will wake the platform by asserting the PME WAKE# line. Once the platform wakes to S0, the Intel WLAN Driver will disable NetDetect until enabled again by the SCT Agent.

5.2.1 NetDetect Flow

NetDetect Enabled:

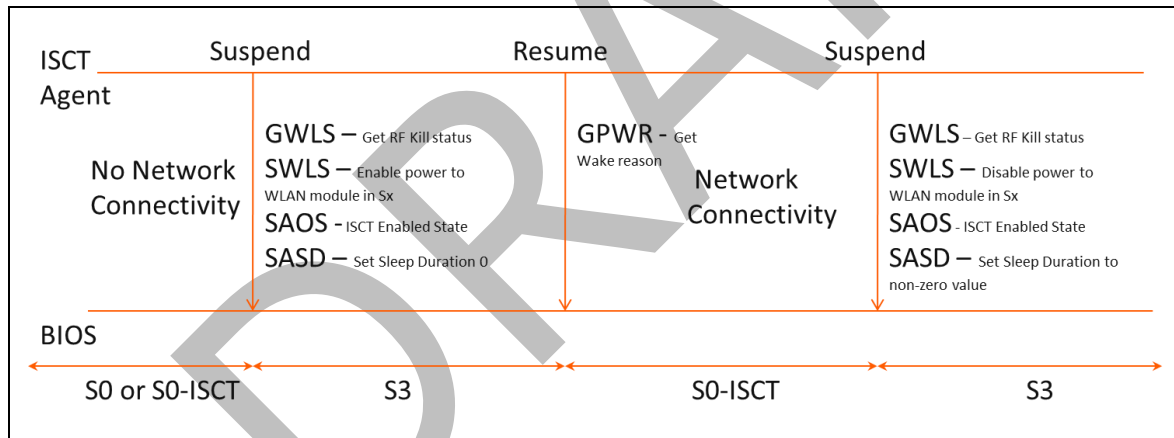
- When in S0 or S0-ISCT, there is no network connectivity, at suspend time, Intel® Smart Connect Technology Agent will disable the timer by setting sleep duration to 0 (if not physical Radio On/Off switch, see section below for more details)
- The Agent will request BIOS/EC enable power to WLAN NIC in Sx state using SWLS ACPI control method
- The Agent will enable NetDetect in WLAN driver

NetDetect Disabled:

- When in S0 or S0-ISCT, if there is network connectivity at suspend time, Agent will enable the timer by setting sleep duration to non-zero value and not enable NetDetect
- The Agent will disable power to WLAN NIC in Sx state using SWLS ACPI control method
- The ISCT Agent will disable NetDetect in WLAN driver

Note: Intel® Smart Connect Technology function status is always enabled (SAOS = 1)

Figure 5-1. NetDetect Flow





5.2.2 NetDetect Behavior on Power Loss

If during the Sx state, power is removed from the WLAN device and later restored while the platform is in S3, a boot of the OS is required to re-load the NetDetect microcode for the WLAN device.

5.2.3 NetDetect Configuration

There are several OEM registry settings that effect NetDetect operation on the platform. They are:

- **NetDetectPref:** Set to 0 to disable NetDetect on a platform that does not have a WLAN card supporting NetDetect (for example Desktop system). Default value is 1 to enable NetDetect on platforms.
- **NDFFSCoExist:** Set to 1 to enable the co-existence of NetDetect and Intel® Rapid Start Technology on the platform. Default value is 0 to disable the enabling of NetDetect in extended hours when the ISCT Agent enabled Rapid Start Technology.

See the [Registry Settings](#) section for more information on creating OEM default registry values.

5.2.4 Radio On/Off Switch Effect on NetDetect

Prior to entering S3, the Intel® Smart Connect Technology Agent calls the OS to obtain the status of the WLAN Radio on/off state. The Agent will look at the value of the GABS ACPI control method bit 6 to determine if the platform has a physical Radio On/Off switch or soft switch.

If the platform has a physical Radio On/Off switch (GABS Bit 6 set to '1') and there is no other network connect (LAN) the following will be observed:

- Radio if OFF before going to suspend:
 - SAOS = 1, SASD = 0 and SWLS = 2 (will cause WLAN NIC to be still powered on)
 - NetDetect will be enabled as WLAN NIC has power prior to entering S3
 - If the user turns the radio ON in S3, the EC will wake the platform and indicate in GPWR ACPI control method the wake method was by EC timer. Platform will enter S0-ISCT to update content.
- Radio is ON in S0 and turned OFF in S3:
 - SAOS = 1, SASD = non-zero and SWLS = 2 (No power to the WLAN NIC)
 - In this case, when in S3, and the radio is turned OFF, EC should stop the timer. Since there is no power to WLAN NIC, NetDetect will not wake the platform).
 - If the user turns the radio ON again in S3, EC should wake the platform after a default time (5 minutes for example) and indicate in GPWR ACPI control method the wake method was by EC timer. Platform will enter S0-ISCT to update content.



If the platform does not have a physical Radio On/Off switch (GABS Bit 6 = '0') but instead has a "soft-switch" and there is no other network connection (LAN), the following will be observed in the ACPI control method values:

- Radio is OFF before going to suspend:
 - SAOS = 1, SASD = 0 and SWLS = 0
 - NetDetect will not be enabled as there is no way to turn on the radio in S3 without returning to S0
 - Platform will not wake by periodic timer
- Radio is ON in S0:
 - OS = 1, SASD = 0 and SWLS = 2 (will cause WLAN NIC to be still powered on)
 - NetDetect enabled

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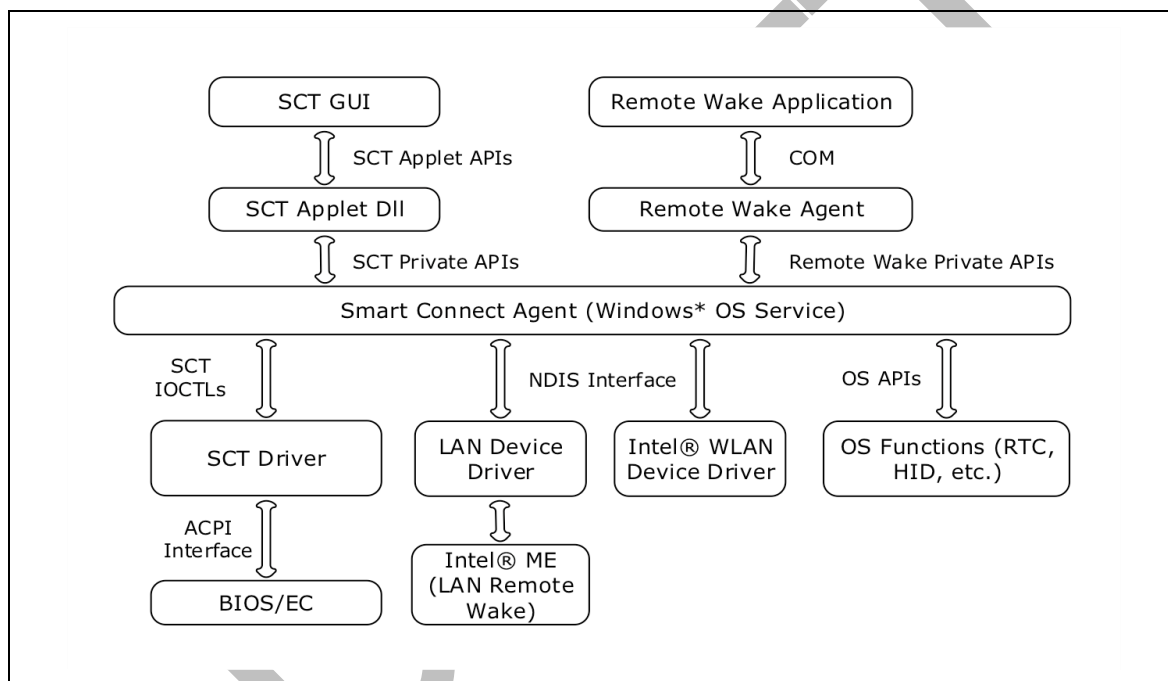
6 Intel® Smart Connect Technology SW

6.1 Overview

Intel® Smart Connect Technology uses OS resident components to manage Intel® Smart Connect Technology operations on the platform.

[Figure 6-1](#) illustrates these components.

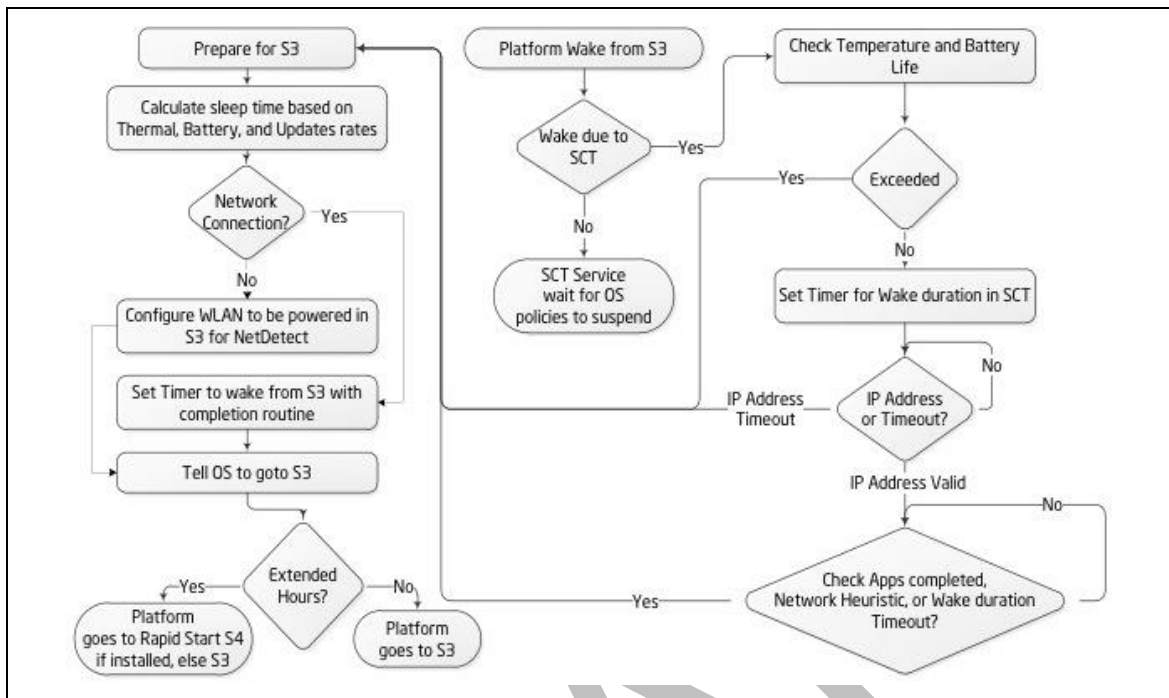
Figure 6-1. Intel® Smart Connect Technology SW Stack



6.2 Intel® Smart Connect Technology Agent

The Intel® Smart Connect Technology Agent controls the Intel® Smart Connect Technology operation on the platform. [Figure 6-2](#) illustrates the operation of the Intel® Smart Connect Technology Agent.

Figure 6-2. Intel® Smart Connect Technology Agent Operation Flow



6.2.1 S0-ISCT Duration on Battery (Normal)

To maintain platform thermals and reduce battery drain, the Intel® Smart Connect Technology Agent limits the amount of time spent in S0-ISCT when the platform is on battery. The S0-ISCT time duration depends upon the following:

- Network Heuristics determine the amount of time spent in S0-ISCT. Currently the Agent Monitor Exchange, IMAP, and POP3 protocols for activity. After detecting inactivity on these protocols, the Agent puts the system back to S3.
 - For IMAP and POP3 protocols, the inactivity period is 10 seconds.
 - For Exchange protocols, the inactivity period is 5 seconds.
- Time constraints derived from previous S0-ISCT sessions:

The maximum S0-ISCT allowable wake time is controlled by a registry value of S0WakeDurationLimitSeconds. This registry value has a default and maximum value.

6.2.1.1 S0-ISCT Duration on AC (Extended)

The S0-ISCT wake duration can be extended by the platform by responding to the GAOS ACPI control method with Bit 1 set to '1'. The Agent will indicate Extended Media S0-ISCT duration is supported by setting Bit 1 of the SAOS ACPI control method to '1'. Extended wake duration allows for larger content download. Once network activity falls below 100KB for 30 seconds, the platform is transitioned back to S3.

For mobile platforms, this is only supported if the platform is on AC.



During the Extended wake duration S0-ISCT session, the BIOS/EC may turn on the fans to allow longer sessions. Depending on the thermal capabilities of the platform, the recommendation is to use the low speed setting of the fans and only when the lid is open or the system is docked.

6.2.2 Network Connectivity Check

Upon entering the S0-ISCT mode, the Intel® Smart Connect Technology Agent performs the following network connectivity checks:

- Check for network connection on available network devices (LAN/WLAN).
- If no network connection, then transition platform back to S3 (enable NetDetect if possible).
- If network connection(s) exists:
 - Set 15 second IP Address Start wait timer to allow sufficient time for network cards to obtain IP address and VPN reconnection.
 - Listen for IP Address change notification.
- If no IP address is available in 15 seconds (IP Address timer expires), transition platform back to S3 (enable NetDetect if possible).
- Verifies internet connection is available

Note: If multiple network connects exist, the OS will determine which one to use (typically based on connection speed).

6.2.3 Thermal and Battery Monitoring during S0-ISCT

The Intel® Smart Connect Technology agent monitors the thermal and battery life (if on battery) during the S0-iSCT session every 1 second. If the Agent detects the CPU temperature has exceeded CPU TjMAX delta for suspend (**ThermalThresholdCentigradeSuspend** (default is 47°C) on DC and **ThermalThresholdCentigradeSuspendAC** (default is 40°C), the platform transitions to S3. If the agent detects the battery level while on battery falls below the threshold **DCBatteryThresholdHalt**, default is 15%). The platform transitions to S3. However, if the total wake duration is less than 20 seconds, the agent will suspend the platform until the system has been awake for about 20 seconds.

If the Agent detects the CPU temperature has exceeded CPU TjMAX delta for over-temperature (**ThermalThresholdCentigrade**; default is 50°C), the next sleep duration will be extended using the thermal back off rule.

6.2.4 Remote Wake Housekeeping Service

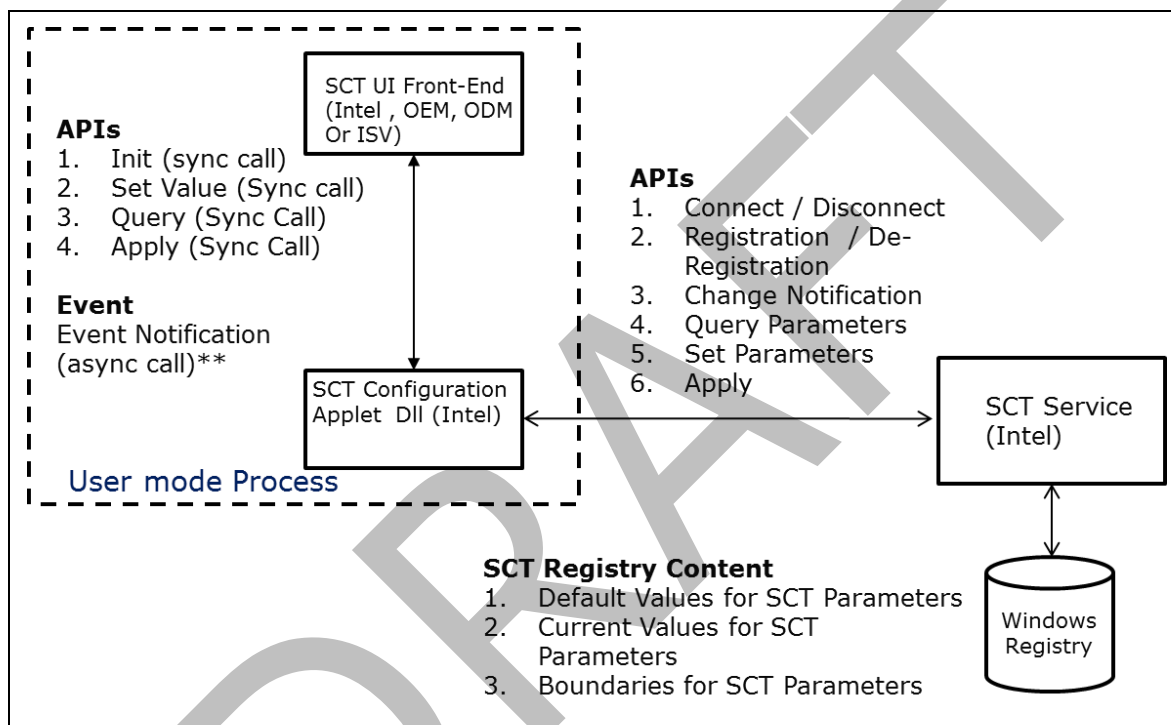
If the LAN or WLAN device is not able to send 5 Keep Alive packets in a row, the device will generate a platform wake (PME#) to reset the network device OS stack and possibly resolve networking issues. This wake will show up in the Intel® Smart Connect Technology log as "Remote Wake Housekeep Service".

In addition to the Keep Alive failure wake, a "once-a-day" wake is also scheduled to occur.

6.2.5 Intel® Smart Connect Technology Agent Configuration

An OEM can configure the Intel® Smart Connect Technology Agent by using the Intel® Smart Connect Technology Configuration Applet Dll. The Dll provides an interface that allows the UI to configure settings for the Intel® Smart Connect Technology Agent. The following figure shows the interaction of the UI with the Intel® Smart Connect Technology Configuration Applet Dll and the Intel® Smart Connect Technology Agent.

Figure 6-3. Configuration Applet



The Intel® Smart Connect Technology Configuration Table below lists all of the various parameters configurable for the Intel® Smart Connect Technology Agent and the associated default value.

To allow OEMs flexibility in their usage of the Intel® Smart Connect Technology, they may wish to provide their own override values in the following registry key:

[HKEY_LOCAL_MACHINE\SOFTWARE\Intel\Intel® Smart Connect Technology\OEM]

The Agent reads the OEM values when it begins execution after a reboot and if the values are within the minimum and maximum ranges, the Agent uses those values instead of the default values that are hard coded in the Agent.

Note: When a registry value is updated, the Intel® Smart Connect Technology Agent must either be restarted or the system rebooted for the values to take effect.



Table 6-1. Intel® Smart Connect Technology Agent Registry Configuration Settings

Name	Registry Key	Min	Max	Default	UI
Daytime Intel® Smart Connect Technology Update Frequency	S3SleepDurationSeconds	15 min	60 min	15 min	✓
Night time Start Range h:mins	NightTimeDuskMinutes	0h 00min	24h 00min	22h 00min	✓
Night time Stop Range h:mins	NightTimeDawnMinutes	0h 00min	24h 00min	6h 00min	✓
Battery Life % before disabling Intel® Smart Connect Technology	DCBatteryThresholdHalt	Current OS Suspend setting	95%	30%	
S0-Maximum-time-wake-duration	S0WakeDurationLimitSeconds	10 sec	180 sec	45 sec	
CPU Thermal Threshold of when ISCT increases sleep duration	ThermalThresholdCentigrade	60°C below TJ-	40°C below TJ-MAX	50°C below TJ-MAX	
Thermal Thread CPU Thermal Threshold	ThermalThresholdCentigradeSuspend	60°C below TJ-	40°C below TJ-MAX	47°C below TJ-MAX	
Thermal Thread CPU Thermal Threshold on AC	ThermalThresholdCentigradeSuspendAC	60°C below TJ-	40°C below TJ-MAX	40°C below TJ-MAX	
WhiteList	OEM/WhiteList		10 entries		
Audio Settings Delay when entering S0-ISCT mode	AudioDelayMilliseconds	0		2000 (2 seconds)	
Periodic Wake Enablement	OEM/PeriodicWakePref	0	1	0	✓
NetDetect Enablement	OEM/NetDetectPref	0	1	1	
NetDetect / Rapid Start Co-existence	OEM/NDFFSCoExist	0	1	0	
Remote Wake Enablement	OEM/RemoteWakePref	0	1	0	✓
Enable logging	LoggingEnabled	0	7	0	
Hide "Updates" in "?" menu	OEM/EnableCheckUpdate	0	1	1	
Logging Level	LoggingLevel	0	16	0	

Wake Duration is the amount of time the system spends in the ISCT-S0 state under normal conditions (no thermal issues).

The values S0-Maximum-time-wake-duration is used to specify the amount of time allowed for a platform to stay in ISCT-S0 due to thermal limitations to ensure skin temps do not exceed Intel thermal guideline.



Expose in UI are the possible values that can be configured in the OEM version of the Intel® Smart Connect Technology UI. The OEM can choose to display a subset of these values to have a simple UI to expose minimal configuration to the user.

6.2.5.1 Application White List

The Intel® Smart Connect Technology provides the ability to control the periodic wake of the platform by checking prior to entering S3 if an application is running from a defined list of applications ("White List"). The Intel® Smart Connect Technology Agent will suspend the S0-ISCT/S3 cycle if none of the applications defined in the white list are running prior going into S3 mode. A non-existent or empty list will indicate that the Intel® Smart Connect Technology Agent will always do the S3/S0-ISCT cycle irrespective of running applications.

The White List is stored in the OS Registry under the key of:

```
[HKEY_LOCAL_MACHINE\SOFTWARE\Intel\Intel® Smart Connect Technology\OEM]
```

in the "WhiteList" string. The list supports a maximum of 10 applications. The ";" separated entries are the application executable name. The list is read at the start of the Intel® Smart Connect Technology Agent execution.

6.2.5.2 Specifying OEM Settings at Installation

The Intel® Smart Connect Technology installation process allows an OEM to specify their default values thru the usage of an INI file at the time that setup.exe is invoked. The installer will read the contents of the "SmartConnect.ini" file from the current directory or the directory of setup.exe and populate the OEM registry key with those values.

Below is an example INI file that enables logging to the ISCT log file, enables NetDetect, NetDetect and Intel® Rapid Start Technology co-existence, enables Remote Wake and sets the default sleep duration to 30 minutes.

```
; this comment must remain as the first visible line in the file

[OEM]

LoggingEnabled=2

LoggingLevel=16

PeriodicWakePref=0

NetDetectPref=1

RemoteWakePref=1

NDDFFSCoExist=1

S3SleepDurationSeconds=1800

WhiteList=REG_MULTI_SZ, "Outlook.exe", "wlmail.exe"
```

Note: the installer does not validate the settings at install time; the Agent does this. If an entry is not a valid entry (e.g. misspelled) or their values are not within the



accepted range, the Agent does not allow the OEM override and displays an error message in the log file. The OEM must keep the first two lines the same as in the above example.

6.2.6 Event Logging

The Intel® Smart Connect Technology uses the OS Event Log to store log information in the "Applications" Log. The "Source" field is "ISCT".

DRAFT

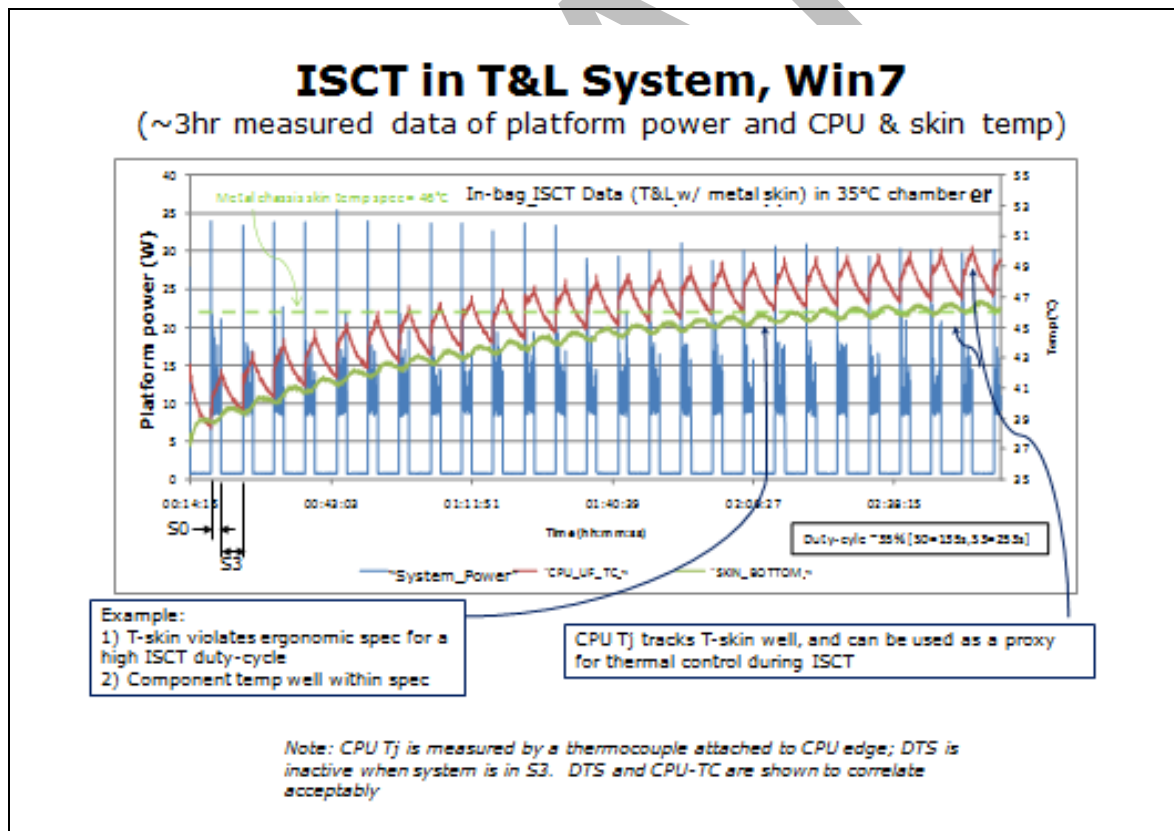
7 Thermal Considerations

7.1 Overview

Illustrative thermal behavior of a notebook platform running Intel® Smart Connect Technology is shown in this section. The examples included here from typical thin-and-light notebook systems are to be used only as illustrations and do not include the entire envelope of system/component/workload variations in the industry.

In Intel® Smart Connect Technology the platform cycles through repeated s-state transitions between sleep and wake, and correspondingly cycles through durations of very low sleep power and relatively higher wake power. Depending upon the duty-cycle of Intel® Smart Connect Technology operation (i.e., a metric to define the duration of wake time compared to sleep time duration in Intel® Smart Connect Technology) the platform can slowly warm up. This warming-up is exacerbated if the system is in thermally challenging environment, like inside a bag, shows an example of such an event.

Figure 7-1. Platform Power & CPU / Skin Temperature Trace Example for 'In-bag' Intel® Smart Connect Technology Usage





Shown in are the measured time traces of platform power (left axis), and CPU and bottom skin hotspot temperatures (right axis) of a typical thin-and-light notebook system that is kept inside a bag inside a thermally controlled 35°C chamber. The total system power was measured after the AC adapter power source. The temperature was read by thermocouples attached to the key components (CPU, Comms, Mem, chipset, etc.) and skin hot spots at top and bottom. Although it is to be expected that the actual junction temperature of components could be couple of degrees higher than the temperatures recorded by attached thermocouples, the overall trends are to be similar and representative. The thermocouple approach used here allowed constant temperature monitoring across platform power states (S0, S3, S4, mechanical off).

In the illustration of S0 duration started with a sharp power spike (~30W in this example), and then maintained a lower active power state (at around ~10-15W) for the remainder of the S0 duration. During S3 the system consumed a much lower sleep power (~0.5W). These S0 and S3 power characteristics are dependent upon various factors, such as, processor p-state, number of active cores, processor architecture type (core or atom platform), the generation of the product, etc.

The notebook system, being thermally constrained inside a bag, fails to dissipate the heat to environment and slowly warms up. The overall warming upward trend can be observed in the figure and depends upon average platform power during Intel® Smart Connect Technology (approximately 4.5W in the current example for a very high duty cycle).

In addition to overall warming trend, in a finer scale, CPU also goes through miniature thermal excursions during every Intel® Smart Connect Technology cycle. This can be seen by localized temperature peaks and valleys, as seen by saw-tooth pattern of CPU TC plot during higher power wake and lower power sleep states. The localized peak values depend upon the p-state limit and wake duration during Intel® Smart Connect Technology, but, exits far away (by few ten's of degrees) from the T_{j,max} specification limit of the product (105°C in this example).

Note that, similar to CPU temperature, other key components (PCH, COMMs, Memory) also experience similar thermal excursions. Even though component temperature specifications are not threatened due to nominal Intel® Smart Connect Technology operation, the skin temperature can easily exceed ergonomic specification. For the current example (a very aggressive duty-cycle of ~35%) the specification violation happens within 3 hours of Intel® Smart Connect Technology onset, and the system temperature keeps on climbing.

Therefore, the thermal considerations for Intel® Smart Connect Technology are control of skin temperature within ergonomic limit (examples: ~46°C for metal skin, ~58°C for plastic skin, ~40°C for a touchscreen display), and control of temperature excursion driven component and platform reliability impact.



7.2 Temperature Sensing and Thermal Control

As can be seen, the thermal risk of skin spec violation is very real during Intel® Smart Connect Technology, and thus should be monitored and controlled in a 'closed-loop'. To monitor we need a skin temperature sensor, and to control we need a mechanism and logic.

7.2.1 Skin Temperature Sensor

For Intel® Smart Connect Technology one can use any existing skin sensor or any other sensor that correlates well to the skin temperature for closed-loop thermal control. From a handful of experimental data (on typical thin-and-light systems) the CPU Tj was found to correlate reasonably well with the skin temperature, and therefore used here as a proxy for skin temperature. It is to be recommended that OEMs verify this correlation for a specific system design beforehand, if the system does not have any dedicated skin temperature sensor.

7.2.2 Thermal Control during Intel® Smart Connect Technology Agent

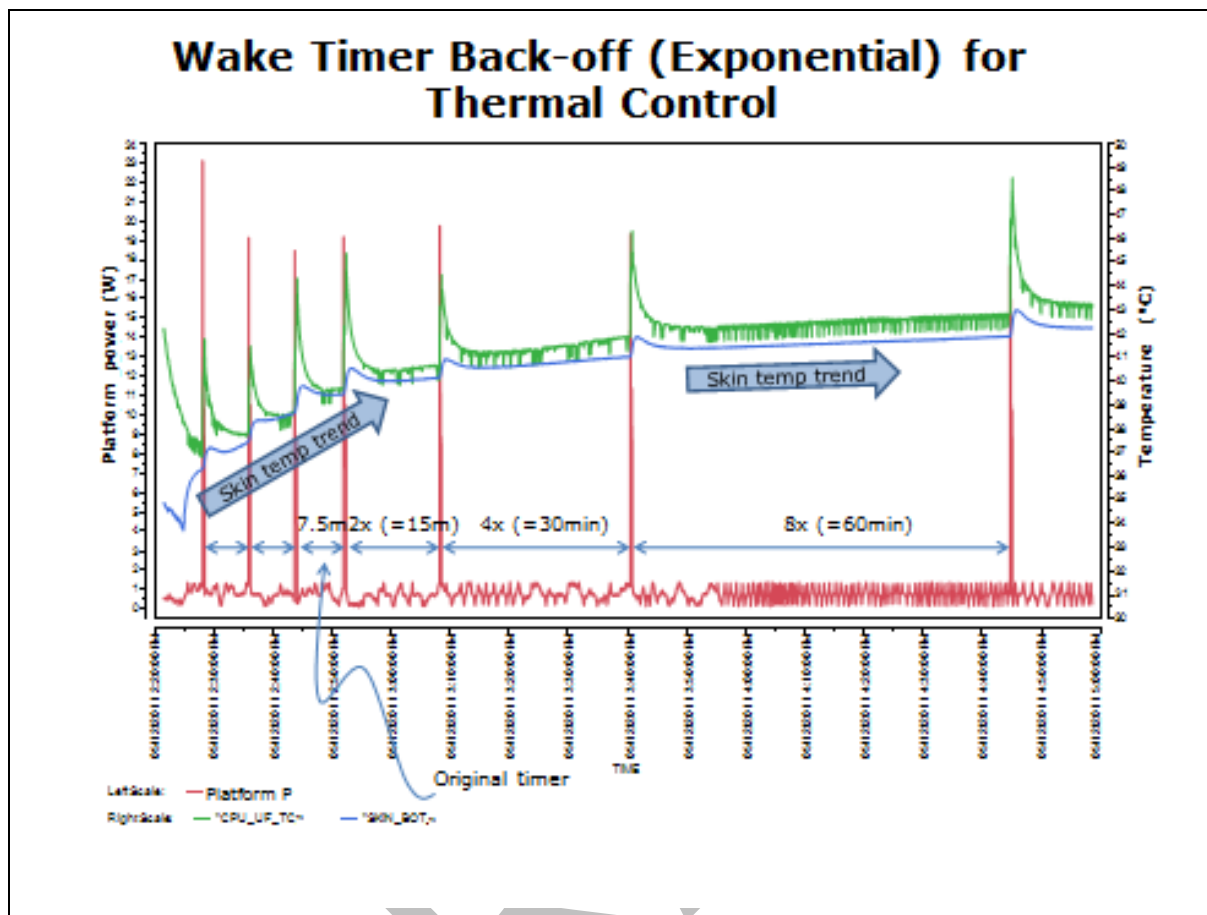
Intel® Smart Connect Technology operates in 'pull-mode' based on a periodic wake timer. By delaying the wake timer while CPU Tj is higher than allowable Intel® Smart Connect Technology threshold a 'closed-loop' thermal control can be achieved by monitoring CPU Tj (as reported by DTS). This allowable Intel® Smart Connect Technology threshold CPU Tj was selected in current example as 50°C to ensure a safe skin temp (ergonomic spec is 46°C for a metal skin).

Note: The allowable Intel® Smart Connect Technology threshold CPU Tj is expected to be considerably lower than CPU Tj,max spec (generally near 100°C).

In this illustration, an ergonomically safe skin temperature was maintained in a closed loop knob by exponentially backing-off the wake-up timer when thermal control was needed. That is, CPU Tj was read near the end of S0-ISCT state, and, if registered higher than the threshold 50°C temperature, the wake timer was to be backed-off to twice the original wake timer. This allowed the platform to cool down with twice the extended s3 period. At the next wake, if CPU Tj still registered higher than the threshold, timer was further backed-off yet another 2-fold. When CPU Tj conformed to threshold limit the original timer was re-instated. [Figure 7-2](#) shows an example of such a thermal control event and skin temperature response.



Figure 7-2. Wake Timer Exponential Back-off Example for Thermal Control



Shown in [Figure 7-2](#) above are the measured time traces of platform power (left axis), and CPU and bottom skin hotspot temperatures (right axis) of a typical thin-and-light notebook system in a bag inside a temperature controlled chamber. The experimental parameters were selected to create a repeat exponential back-off situation and demonstrate that it can effectively help in thermal control. In this example, back-off mechanism kept on triggering, and continually delayed the original wake timer of 4 min to 8 min, 16 min, and 32 min and was able to cease the increase of system's skin temperature.

7.2.3 Initial Thermal Related Delay for Intel® Smart Connect Technology Agent

It was also observed that when a hot system, after a prolonged intense operation under an aggressive workload, is inserted immediately into a bag, the skin actually warms up for several minutes (found to be up to 5 minutes for this example of thermally aggressive environment), before starting to cool, and coming back to ergonomically safe limit. This is because of heat redistribution from hotter inner components to relatively cooler skin. Therefore it is recommended that OEMs imply an algorithm to delay entrance to Intel® Smart Connect Technology mode to let platform cool for at least 5 minutes.



7.2.4 Summary of Intel® Smart Connect Technology Thermal Control

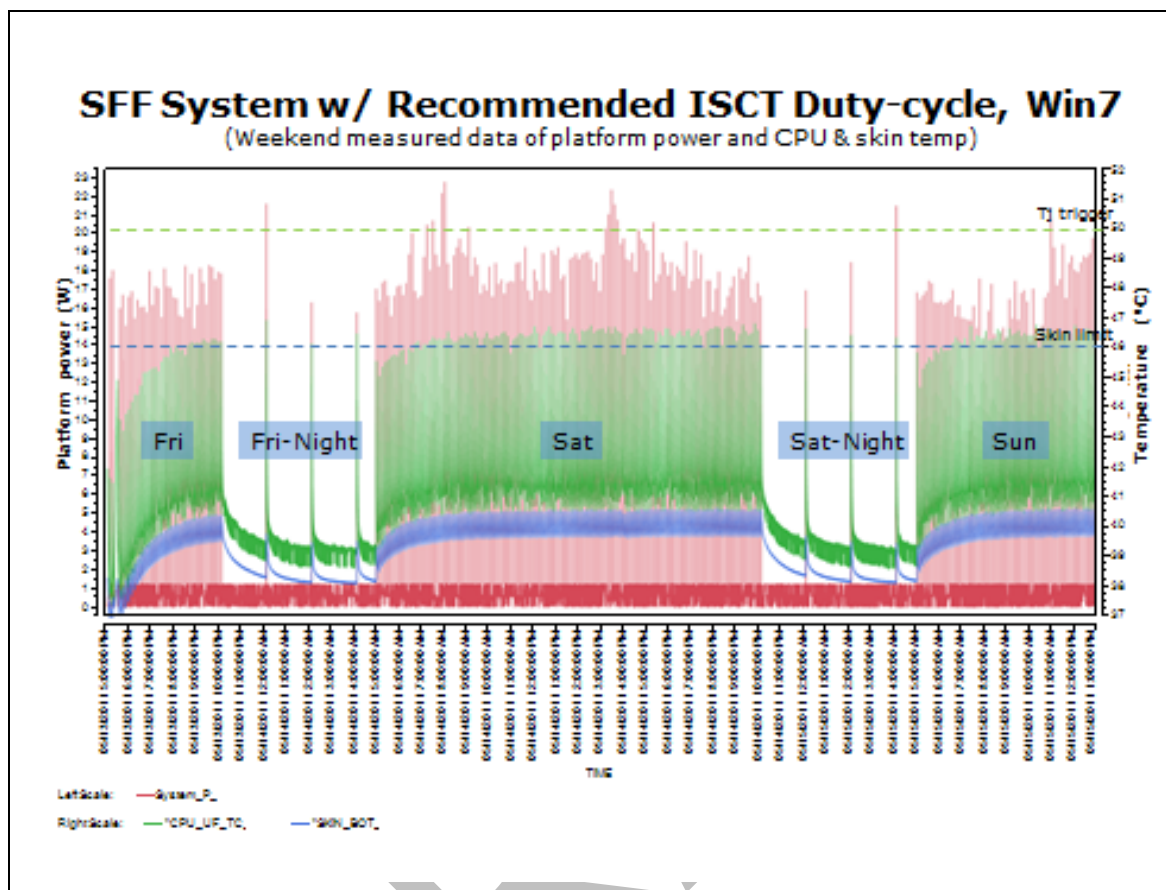
For the runs shown, the following conditions help maintain thermal control of the system while running the Intel® Smart Connect Technology Agent.

- Average (S3 and S0 average) Intel® Smart Connect Technology power limit of 2W for thin-and-light notebooks (can be scaled down by 15% for an ultrathin SFF system).
- CPU p-state limited to lowest state (Pn) during Intel® Smart Connect Technology operation.
- Intel® Smart Connect Technology software limits duty-cycle within 10% for T&L systems.
- Exponential back-off algorithm based on CPU Tj threshold of 50°C with a limit of 1 hour back off.
- S0-ISCT max duration limit, 1 time max = 165s, consecutive max = 45s.
- Initial delay of 5 minutes for Intel® Smart Connect Technology start to aid platform cooling.

When the Intel® Smart Connect Technology runs with the above constraints, an intended and uninterrupted operation was observed for an extended period. An illustration of this is shown in [Figure 7-3](#).



Figure 7-3. Example of Intel® Smart Connect Technology Operation over Extended Period



Additionally, OEMs should consider the following recommendations:

- Turn fan off during in-bag Intel® Smart Connect Technology operation. Fan does not help in-bag cooling, but can cause harm by plugging platform vents with lint.
- If in-bag battery charging is detected, slow down Intel® Smart Connect Technology events to prevent platform heating.

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8 Intel® ME Co-existence with Intel® Smart Connect Technology

Note: This section is not applicable to Bay Trail M/D

Intel® Smart Connect Technology does not have any dependency on Intel® MEI driver or Intel ME FW. Additionally Intel® Smart Connect Technology does not consume SPI flash space. The Intel® Centrino® Wireless NetDetect feature is resident in the FW of the WLAN NIC.

8.1 WLAN Power Control

Because Intel® Smart Connect Technology requires power to the WLAN card in Sx when the Intel® Smart Connect Technology Agent requests the power to be turned on, the EC of the platform should ignore Intel® ME requests to turn off the power to the WLAN card and respond appropriately to the IA32 ACPI method SWLS when it is called by the Agent to turn on/off power to the WLAN in Sx.

8.2 Intel® ME WLAN Provisioning

When the Intel® ME is provisioned for WLAN support during its M3 state and the platform is on AC, NetDetect will not be functional as the Intel® ME will take ownership of the WLAN card. The following table illustrates the Intel® ME states and provisioning modes that effect Intel® Smart Connect Technology and NetDetect operations.

Note: For NetDetect to be enabled when the system is on DC (battery), the system must be switched to battery before the system is placed in S3.

Table 8-1. Intel® ME Co-existence with Intel® Smart Connect Technology (applies to series 9 chipsets only)

System State	Intel® ME State	Intel WLAN Ownership	Provisioned	Power State	Intel® Smart Connect Technology		
					Periodic Wake	Remote Wake	NetDetect Available
S0	M0	Host	Yes	AC	Yes	Yes	Yes
S3	M3	ME	Yes	AC	Yes	Yes	Yes
S4	M3	ME	Yes	AC	Yes	Yes	No
S3	Moff	Host	Yes	AC	Yes	Yes	Yes
S4	Moff	Host	Yes	AC	Yes	Yes	No
S3	Moff	Host	Yes	DC	Yes	No	Yes
S4	Moff	Host	Yes	DC	Yes	No	No
S3	M3	Host	No	AC	Yes	Yes	Yes



System State	Intel® ME State	Intel WLAN Ownership	Provisioned	Power State	Intel® Smart Connect Technology		
					Periodic Wake	Remote Wake	NetDetect Available
S4	M3	Host	No	AC	Yes	Yes	No
S3	Moff	Host	No	DC	Yes	No	Yes
S4	Moff	Host	No	DC	Yes	No	No

8.3 WakeOn LAN (WOL) Support (RW)

Remote Wake via Intel LAN device is managed by the Intel® ME in the S3/S4 state. The Intel® ME requires M3 support to remain on in the S3/S4 state. Refer to the selected platforms Platform Design Guide for more information on enabling M3 support for the Intel® ME.